Linux/Android FS/MM/Storage Workshop

#### Application Performance Profiling using Blocked Samples

#### 02/18/2025

#### Jinkyu Jeong

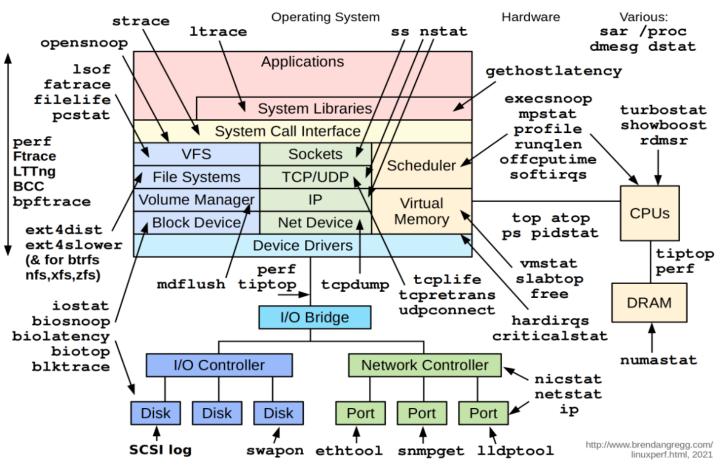
Department of Computer Science and Engineering



#### **Linux Performance Analysis**

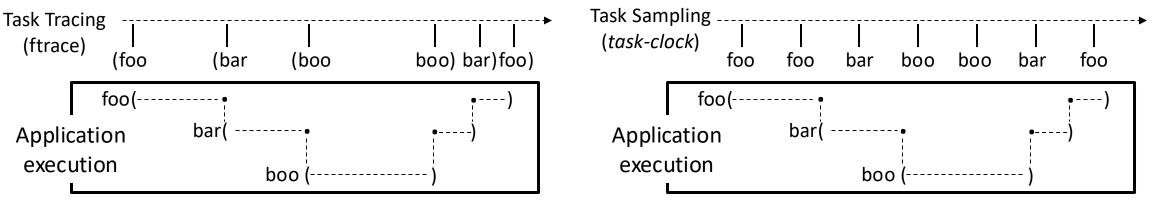
- Various system tools for different system events
  - Application
  - -OS
  - Blocking I/O (device ops.)
- Linux *perf* is widely (and generally) used performance profiling tool

#### Linux Performance Observability Tools



#### Linux perf Subsystem

- Supports performance profiling through collecting execution information
  - Collects IP and callchain
- Tracing vs. Sampling
  - Tracing(=instrument): collects every events
    - e.g., Linux ftrace or tracepoints
  - Sampling: collects samples of events periodically
    - e.g., Linux perf record task-clock





# Linux perf Sampling (task-clock)

- Linux perf sampling co-operates with the periodic timer (i.e., HR timer)
  - e.g., \$perf record -g -e task-clock -c 1000000 ./a.out
    - '-g': callchain, '-e task-clock': event to collect, '-c 1000000': period (=1ms)

```
void func_a() {
  while (i < 2000000)
                            i++;
void func_b() {
  while (i < 4000000)
                            i++;
int main (int argc, char *argv[]) {
 func a();
 func b();
  return 0;
```

7fe0cla2ddbblibc_start_call_main+0x6b (/usr/local/lib/glibc-testing/lib/libc.so.6) 7fe0cla2de76libc_start_main@@GLIBC_2.34+0x86 (/usr/local/lib/glibc-testing/lib/libc.so.6) 55fda5e0d065 _start+0x25 (/home/mw/benchmarks/a.out) <example (\$perf="" of="" sample="" script)="" single=""></example>
<pre>Samples: 2K of event 'task-clock', Event count (approx.): 292000000 Overhead Command Shared Object Symbol - 67.12% a.out a.out [.] func b</pre>
func_b main libc_start_call_main libc_start_main@@GLIBC_2.34 start
- 32.84% a.out a.out [.] func_a func_a main libc_start_call_main libc_start_main@@GLIBC_2.34 start

<Statistical analysis result (\$perf report)>

#### Sampling-Based Profilers (1/2)

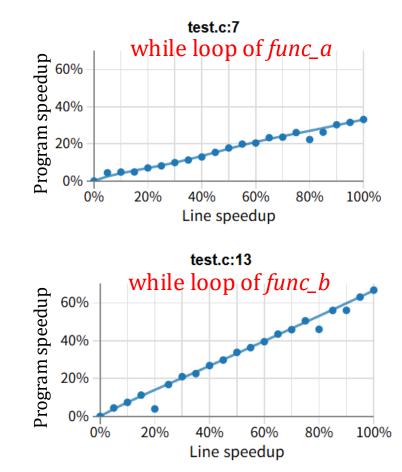
- FlameGraph [Brendan Gregg]
  - <u>Callstack visualization</u> of sampling results

F	Flame Graph	
func_a func_b		
main		
libc_start_call_main libc_start_main@@GLIBC_2.34		
_start		
a.out		

# Sampling-Based Profilers (2/2)

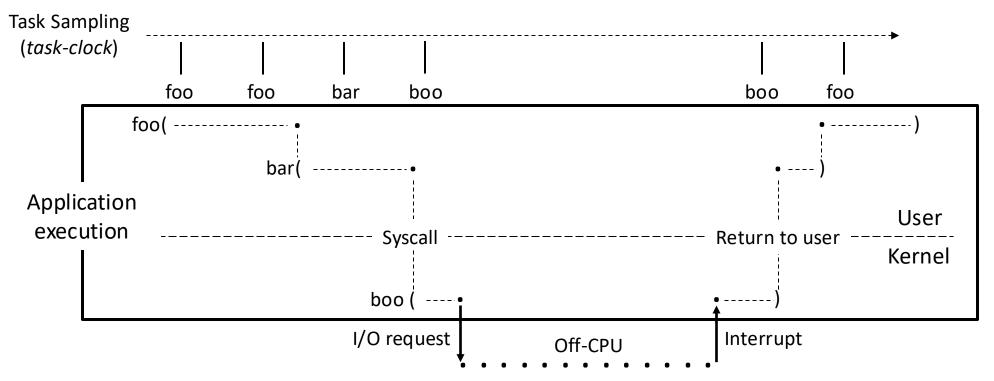
- COZ [SOSP '15]
  - Predict the impact of optimizing without actual optimization

```
void func_a() {
  while (i < 2000000)
                             i++;
void func_b() {
  while (i < 4000000)
                             i++;
int main (int argc, char *argv[]) {
  func a();
  func b();
  return 0;
```



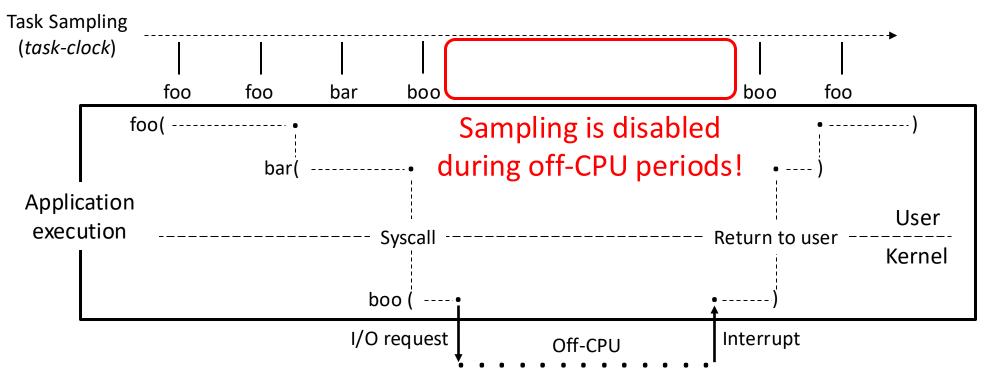
#### **Research Question**

- Is sampling effective for real-world applications?
  - Can sampling handle off-CPU events (e.g., blocking I/O, CPU scheduling, locks)?



#### **Research Question**

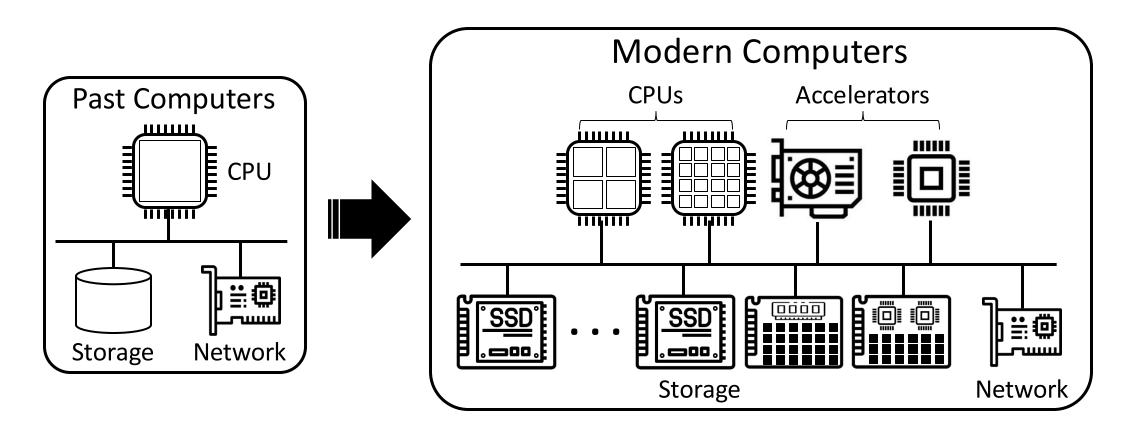
- Is sampling effective for real-world applications?
  - Can sampling handle off-CPU events (e.g., blocking I/O, CPU scheduling, locks)?



No! Sampling cannot collect off-CPU information - Why the off-CPU analysis is important?

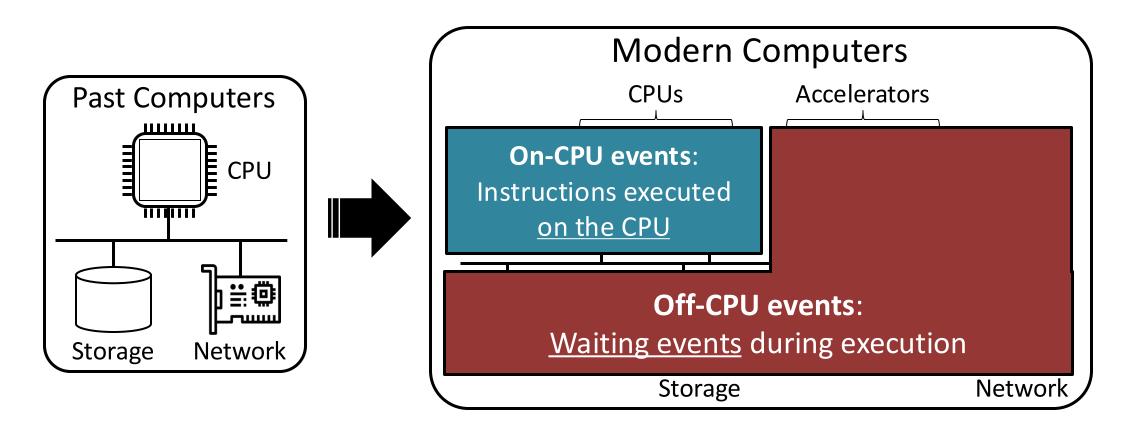
#### **Trend of Computing Environments**

- Computing environments are becoming more complex and advanced
  - Events executed outside the CPU (i.e., <u>off-CPU</u>) have become more diverse



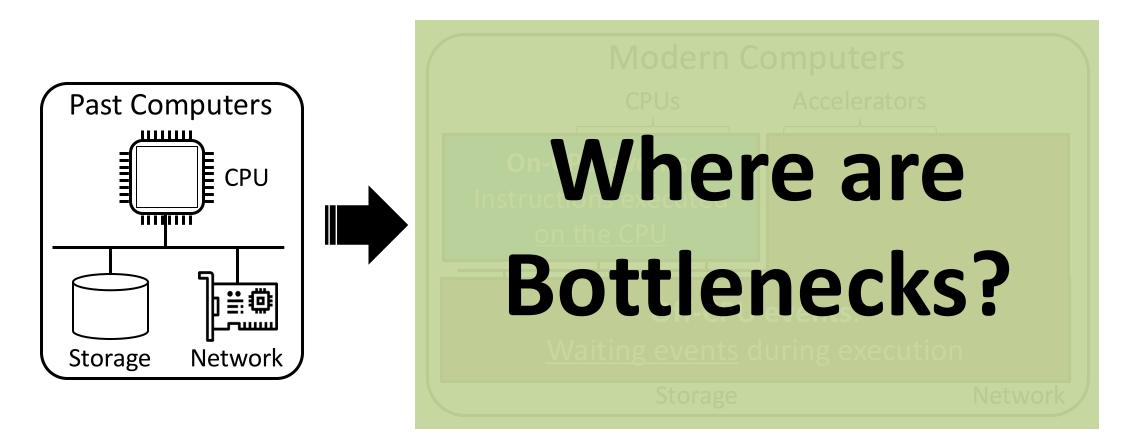
#### **Trend of Computing Environments**

- Computing environments are becoming more complex and advanced
  - Events executed outside the CPU (i.e., off-CPU) have become more diverse

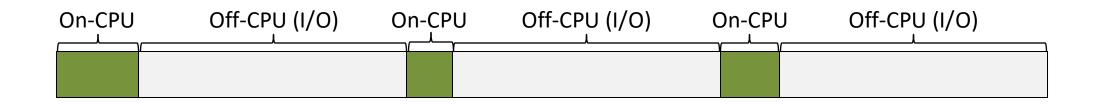


#### **Trend of Computing Environments**

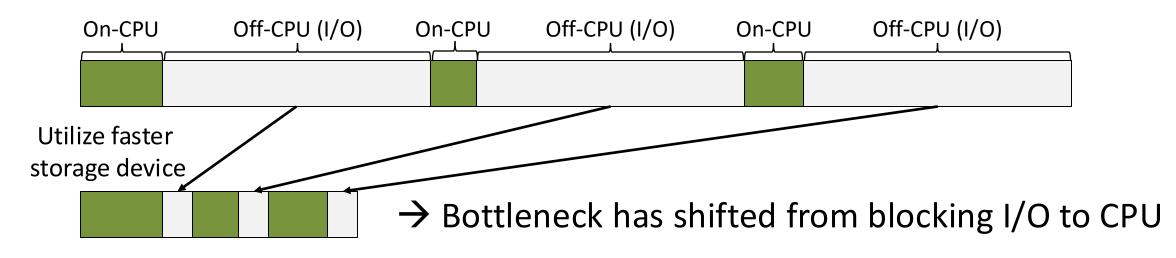
- Computing environments are becoming more complex and advanced
  - Events executed outside the CPU (i.e., off-CPU) have become more diverse



- Bottlenecks of applications are diversifying
  - (I/O) Boundary between CPU-bound and I/O-bound is blurred



- Bottlenecks of applications are diversifying
  - (I/O) Boundary between CPU-bound and I/O-bound is blurred

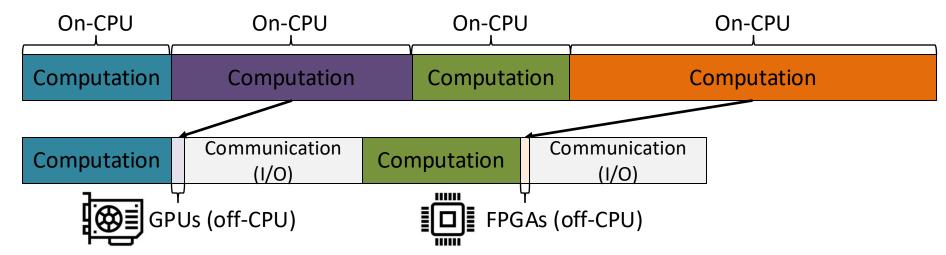


- "kernel software is becoming the bottleneck", XRP [OSDI '22]
- "server CPU is becoming the bottleneck", XSTORE [OSDI '20]
- "Rocksdb is CPU-bound", Kvell [SOSP '19]
- "kernel I/O stack accounts for a large fraction", AIOS [ATC '19]
- "storage no longer being the bottleneck", uDepot [FAST '19]

- Bottlenecks of applications are diversifying
  - (I/O) Boundary between CPU-bound and I/O-bound is blurred
  - (<u>Computation</u>) Shifting away from CPU-centric computations

On-CPU	On-CPU	On-CPU	On-CPU Computation	
Computation	Computation	Computation		

- Bottlenecks of applications are diversifying
  - (I/O) Boundary between CPU-bound and I/O-bound is blurred
  - (<u>Computation</u>) Shifting away from CPU-centric computations

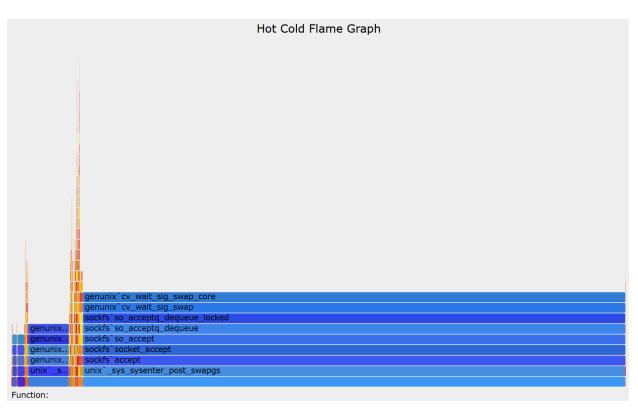


 $\rightarrow$  Bottleneck has shifted from CPU computation to I/O and communication

- "there are spare CPU and network bandwidth", BytePS [OSDI '20]
- "rapid increases in GPU will shift the bottleneck towards communication", PipeDream [SOSP '19]
- "DNN training is not scalable, mainly due to the communication overhead", ByteScheduler [SOSP '19]

### Off-CPU Analysis (1/2)

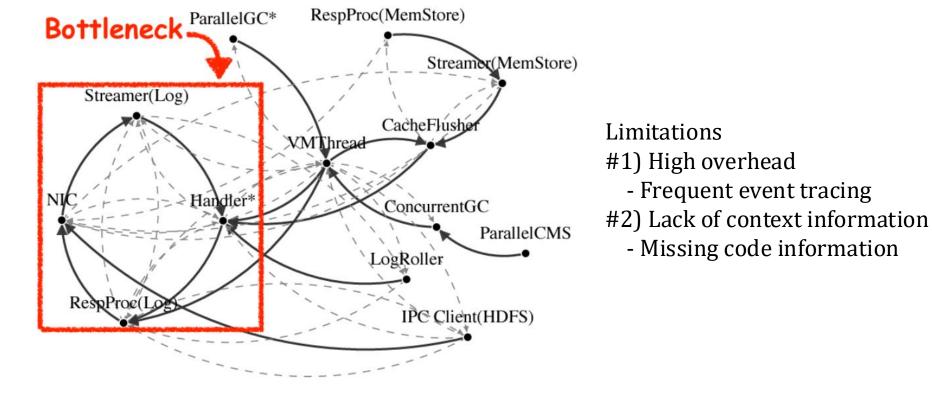
- Existing off-CPU analysis relies on tracing
  - Hot/Cold FlameGraph [Brendan Gregg]
    - Traces all blocking events (i.e., schedule-in/out) using Linux perf subsystem



<Example of hot/cold FlameGraph>

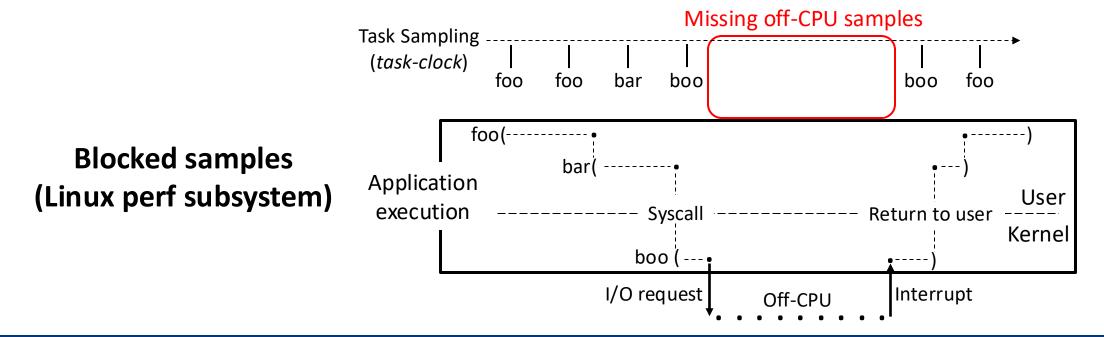
### **Off-CPU Analysis (2/2)**

- Existing off-CPU analysis relies on tracing
  - wPerf [OSDI '18]
    - Traces all waiting events between threads with their dependency



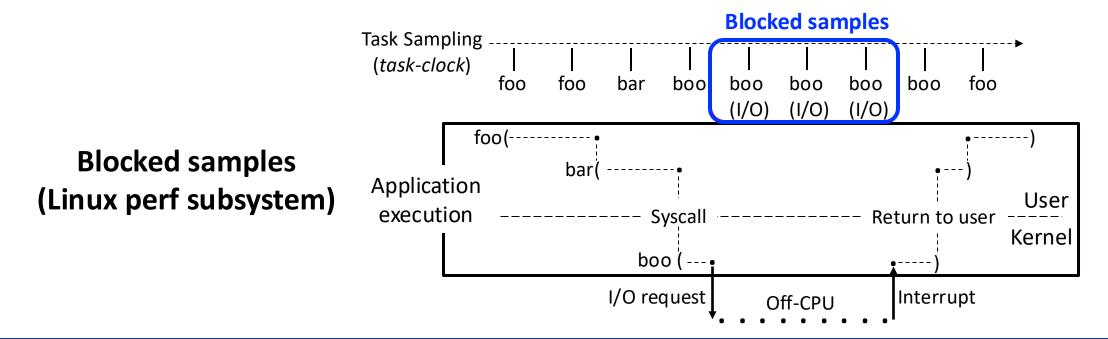
#### **Our Approach: Blocked Samples**

• Goal: sampling on- and off-CPU events simultaneously



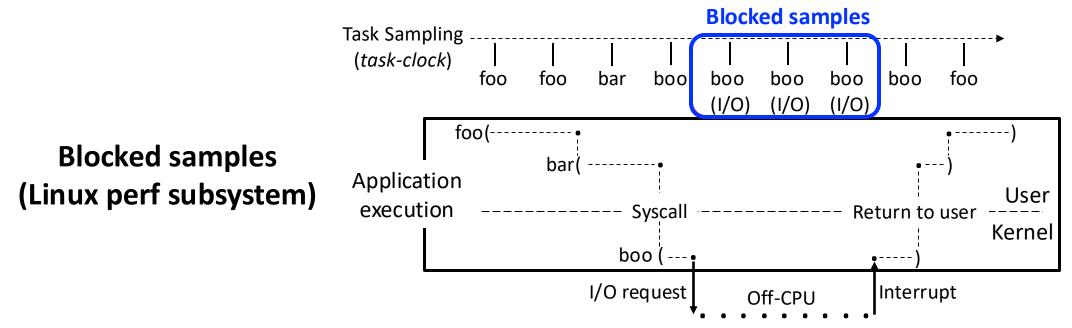
#### **Our Approach: Blocked Samples**

- Goal: sampling on- and off-CPU events simultaneously
  - **Blocked samples:** sampling technique for off-CPU events (task-clock-plus)



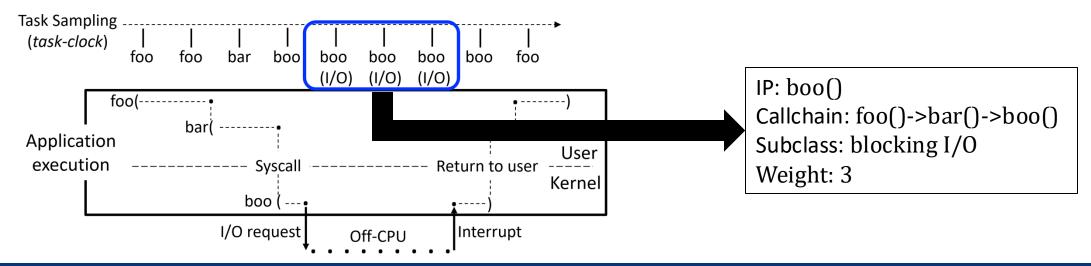
#### **Our Approach: Blocked Samples**

- Goal: sampling on- and off-CPU events simultaneously
  - Blocked samples: sampling technique for off-CPU events (task-clock-plus)
  - Proposed profilers using blocked samples
    - **bperf**: sampling-based <u>statistical profiler</u> on both on-/off-CPU events
    - BCOZ: causal profiler that supports virtual speedup on both on-/off-CPU events



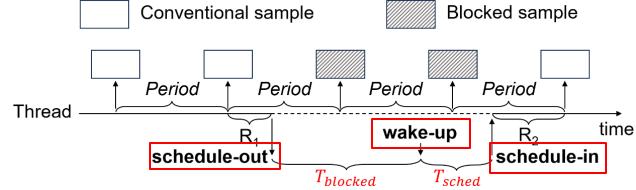
#### **Blocked Samples (***task-clock-plus***)**

- Collected information
  - IP and callchain
  - Off-CPU subclass: reason for the blocking
    - Blocking I/O, synchronization, CPU scheduling, etc.
      - New subclasses can be defined as needed
  - Weight: # of repeats
    - Encode the number of blocked samples with the same attributes



#### task-clock-plus Implementation

• Extending task-clock event in the Linux perf subsystem

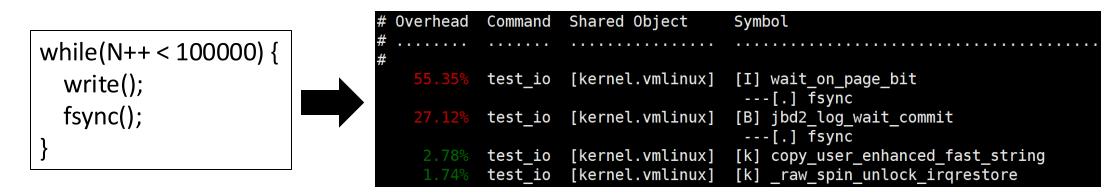


- Hooks in scheduling-related operations
  - Sched-out (prepare\_task\_switch→task\_clock\_event\_del)
    - Records timestamp, and off-CPU subclass
  - Wake-up (*try\_to\_wake\_up*)
    - Records timestamp
  - Sched-in (*finish\_task\_switch*→*task\_clock\_event\_add*)
    - 1) Calculate the length of blocking period
    - 2) Calculate the number of off-CPU samples to record
    - 3) (If exists) Record the off-CPU samples

\* Samples are recorded only if sampling points are overlap with off-CPU period
→ Differ from tracing

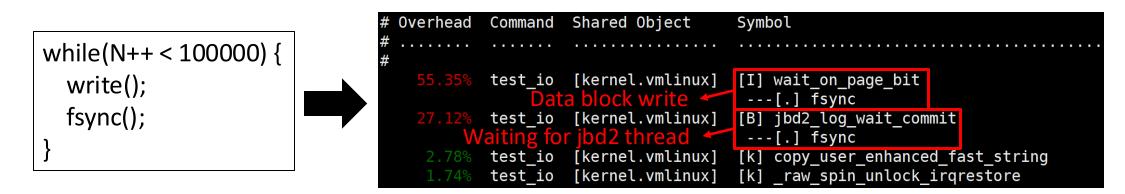
#### *bperf*: Statistical Profiler on Both On-/Off-CPU Events

- Extension of Linux perf tool to support blocked samples
  - Sample accounting
  - Result reporting
    - [I]: blocking I/O, [L]: synchronization, [S]: CPU scheduling, [B]: others
    - Both the last user-level IP and last kernel-level IP are reported for blocked samples
      - Enables an in-depth understanding of off-CPU events



### *bperf*: Statistical Profiler on Both On-/Off-CPU Events

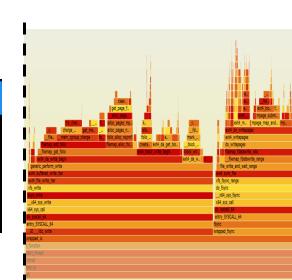
- Extension of Linux perf tool to support blocked samples
  - Sample accounting
  - Result reporting
    - [I]: blocking I/O, [L]: synchronization, [S]: CPU scheduling, [B]: others
    - Both the last user-level IP and last kernel-level IP are reported for blocked samples
      - Enables an in-depth understanding of off-CPU events



### **Toy Program with Mixed of On-/Off-CPU Events**

	<toy p<="" th=""><th>rogram</th><th><pre>&gt; while (i &lt; 300 write(); fsync();</pre></th><th>000) {</th></toy>	rogram	<pre>&gt; while (i &lt; 300 write(); fsync();</pre>	000) {
			}	
Sa				ent count (approx.): 3262000000
	<b>Overhead</b>	Command	Shared Object	Symbol
+	10.70%	test_io	[kernel.kallsyms]	<pre>[k] _raw_spin_unlock_irqrestore</pre>
+	6.52%	test_io	[kernel.kallsyms]	[k] _raw_spin_unlock_irq
+		test io	[kernel.kallsyms]	[k] try_charge_memcg
+	3.72%	test_io	[kernel.kallsyms]	[k] rcu_read_unlock
+	3.70%	test_io	[kernel.kallsyms]	[k] clear_page_erms
+	2.77%	test_io	[kernel.kallsyms]	[k] rep_movs_alternative
+	2.54%	test_io	[kernel.kallsyms]	[k] get_mem_cgroup_from_mm

<w/o blocked samples>



Missing samples → 100% kernel I/O stack

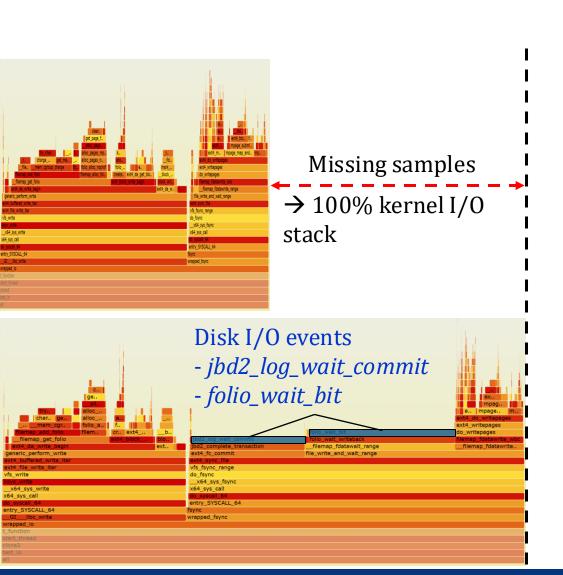
### Toy Program with Mixed of On-/Off-CPU Events

<toy p<="" th=""><th>orogram&gt;</th><th>• while (i &lt; 300 write(); fsync(); }</th><th>000) {</th><th></th></toy>	orogram>	• while (i < 300 write(); fsync(); }	000) {	
Samples: 32	K of event	'task-clock', Eve	e <mark>nt cou</mark> nt	t (approx.): 32620000000
<b>Overhead</b>	Command S	hared Object	Symbol	
+ 10.70%	test_io [	kernel.kallsyms]		aw_spin_unlock_irqrestore
+ 6.52%	test_io [	kernel.kallsyms]		aw_spin_unlock_irq
+ 5.37%	test_io [	kernel.kallsyms]	[k] try	/_charge_memcg
+ 3.72%	test_io [	kernel.kallsyms]	[k]r	<pre>rcu_read_unlock</pre>
+ 3.70%	test_io [	kernel.kallsyms]	[k] cle	ear_page_erms
+ 2.77%	test_io [	kernel.kallsyms]		_movs_alternative
+ 2.54%	test_io [	kernel.kallsyms]	[k] get	mem_cgroup_from_mm

<w/o blocked samples>

#### CPU: 55%, IO wait: 25.4%, Idle(jbd2 wait): 19.7%

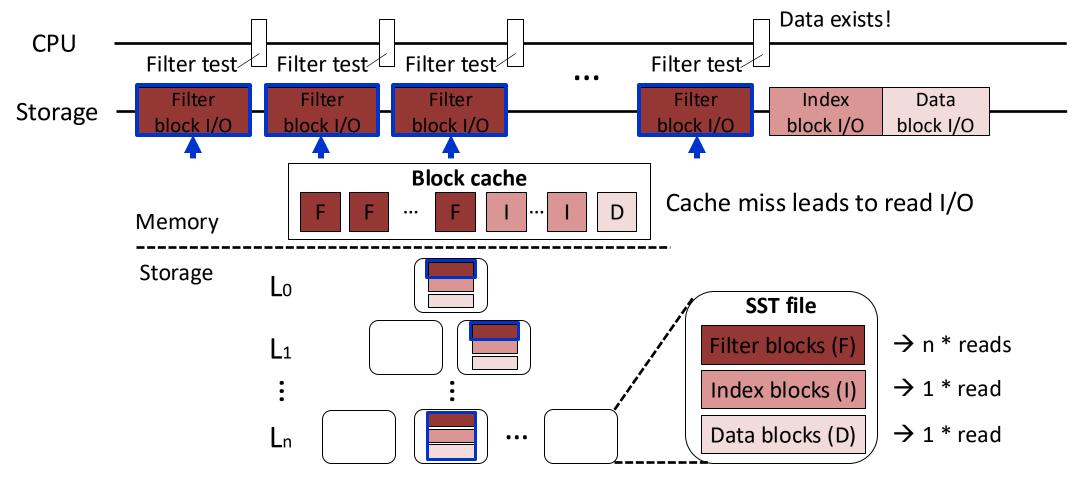
Sa			t 'task-clock-plus' Shared Object	, Event count (approx.): 56140000000 Symbol
+			[kernel.kallsyms]	
+				[B] jbd2 log wait commit
+			[kernel.kallsyms]	[k] _raw_spin_unlock_irqrestore
+			[kernel.kallsyms]	[k] raw spin unlock irq
+	3.05%	test io	[kernel.kallsyms]	
+	2.21%	test_io	[kernel.kallsyms]	[k]rcu_read_unlock
+	1.92%	test_io	[kernel.kallsyms]	[k] clear_page_erms
+	1.53%	test_io	[kernel.kallsyms]	[k] rep_movs_alternative
+	1.39%	test_io	[kernel.kallsyms]	[k] get_mem_cgroup_from_mm



Jinkyu Jeong, Linux/Android FS/MM/Storage Workshop

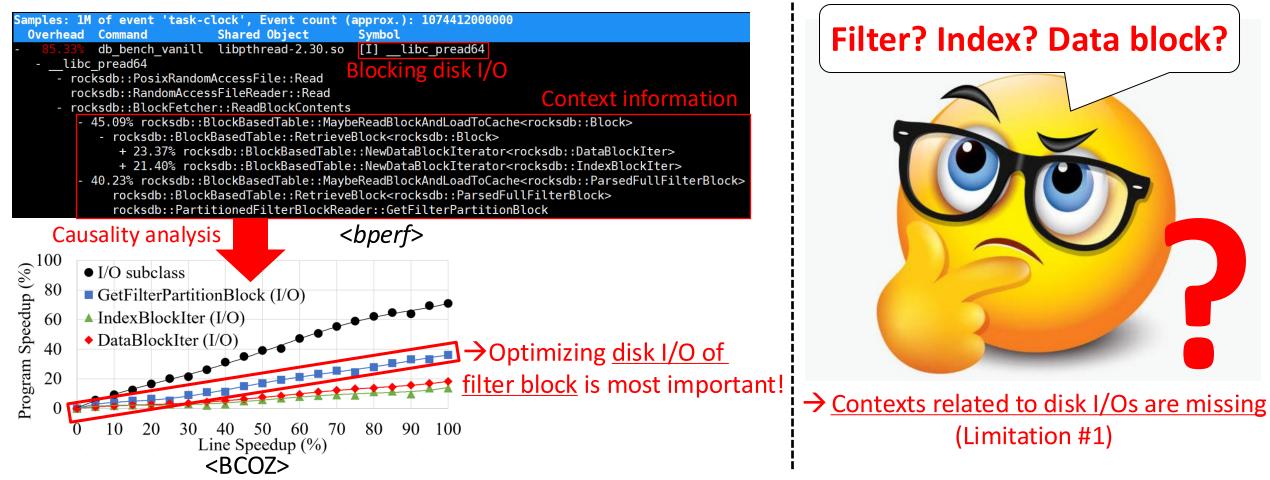
#### **Case Study – RocksDB (Block Read Operation)**

- Scenario: read-only workload (allrandom), small block cache (0.1% of dataset size)
- Problem: frequent block (filter, index, data) read I/Os



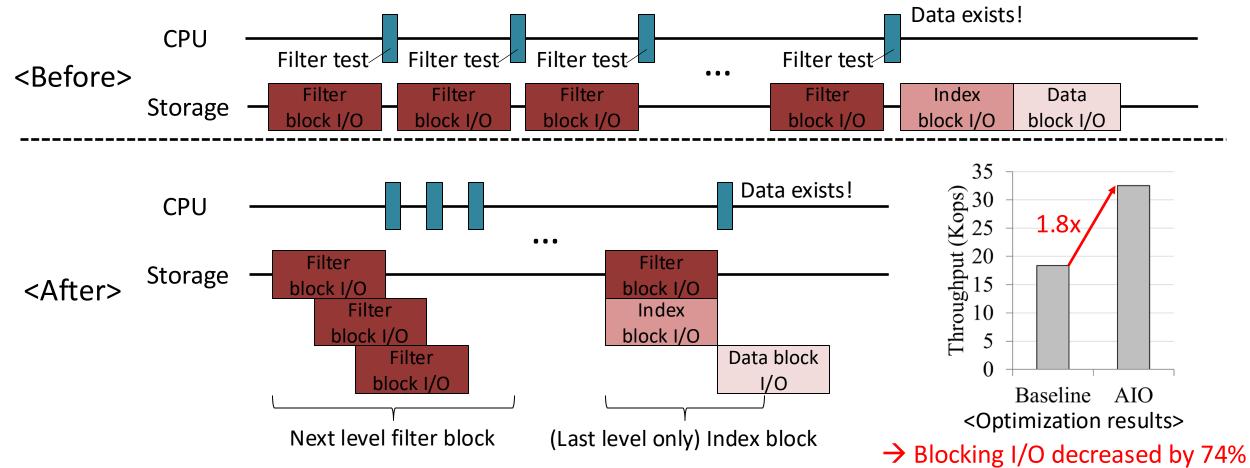
#### **Case Study – RocksDB (Block Read Operation)**

- Scenario: read-only workload (allrandom), small block cache (0.1% of dataset size)
- Identified bottlenecks: blocking disk I/O (filter, index, and data blocks)

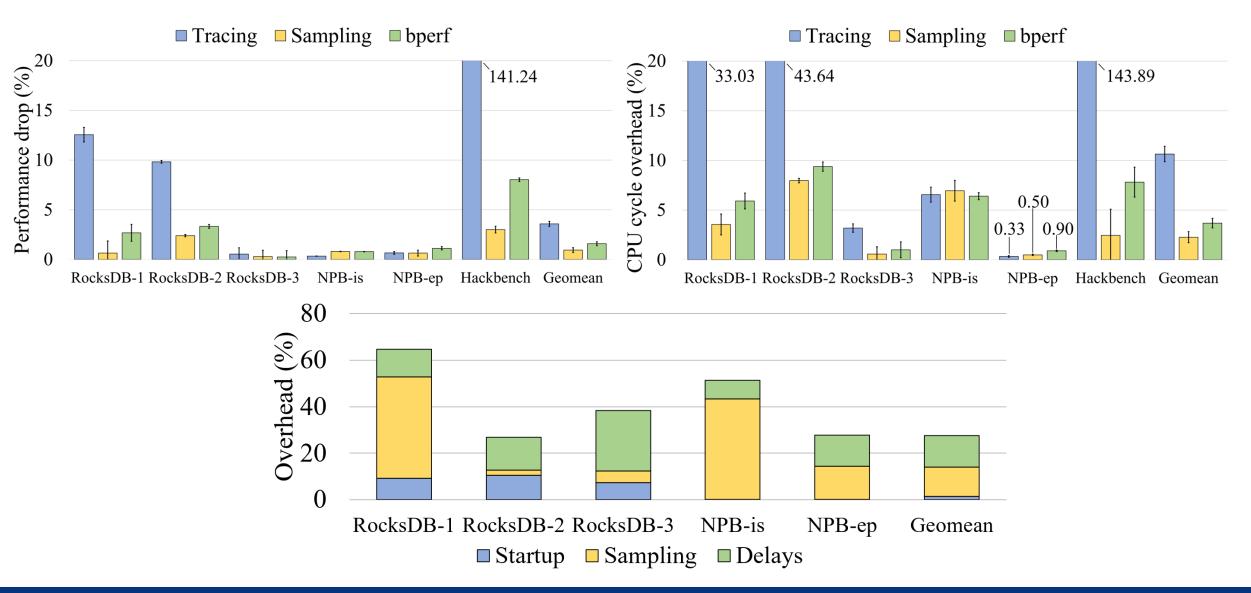


#### **Case Study – RocksDB (Block Read Operation)**

- Scenario: read-only workload (*allrandom*), small block cache (0.1% of dataset size)
- Optimization: asynchronous I/O for filter and index blocks



#### **Profiling Overhead**



#### Conclusion

- Profiling modern applications has become more challenging
- Blocked samples collects off-CPU events information
  - *bperf*, provides <u>statistical profiling</u> of both on-/off-CPU events
  - **BCOZ**, provides <u>virtual speedup</u> of both on-/off-CPU events

Blocked samples is available at: <u>https://github.com/s3yonsei/blocked\_samples</u> <u>https://github.com/s3yonsei/linux-blocked\_samples</u>

# Thank you!

Credit:

Minwoo Ahn, Jeongmin Han, Youngjin Kwon, Jinkyu Jeong, "Identifying On-/Off-CPU Bottlenecks Together with Blocked Samples," OSDI 2024

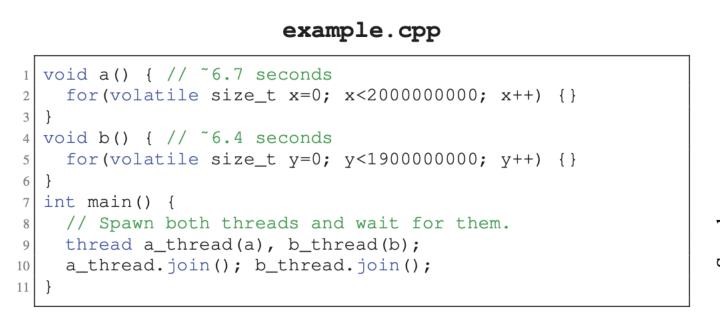


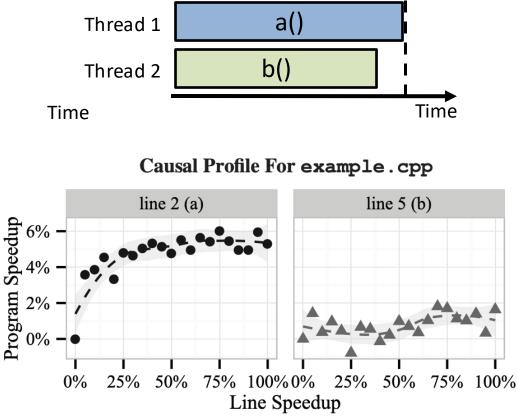
#### Credit

- Minwoo Ahn, Jeongmin Han, Youngjin Kwon, Jinkyu Jeong, "Identifying On-/Off-CPU Bottlenecks Together with Blocked Samples," OSDI 2024
- Most slides are from the OSDI'24 presentation slides

#### **COZ (SOSP '15)**

- COZ: Finding Code that Counts with Causal Profiling, SOSP '15
  - Charlie Curtsinger, Emery D. Berger

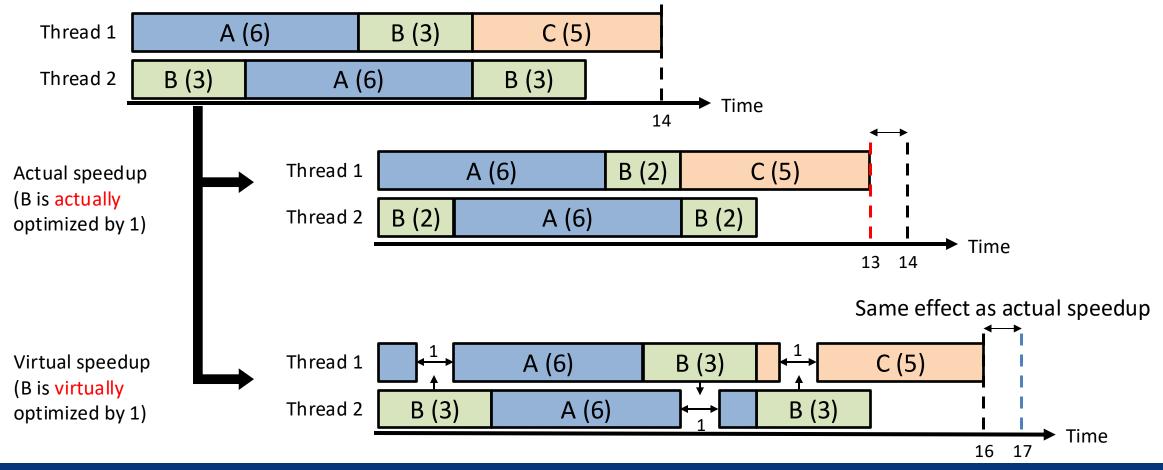




(b) Causal profile for example.cpp

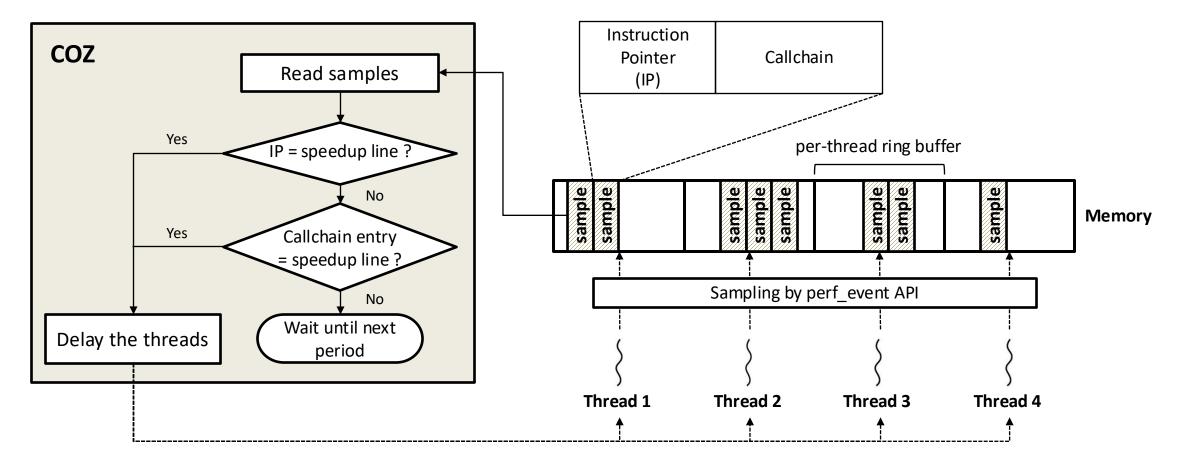
### COZ (SOSP '15)

- Virtual speedup
  - Predict speedup of functions without actually speeding up code lines



#### COZ (SOSP '15)

- COZ is causal profiler using the virtual speedup technique
  - perf sampling + batch processing + thread sleeping and synchronization

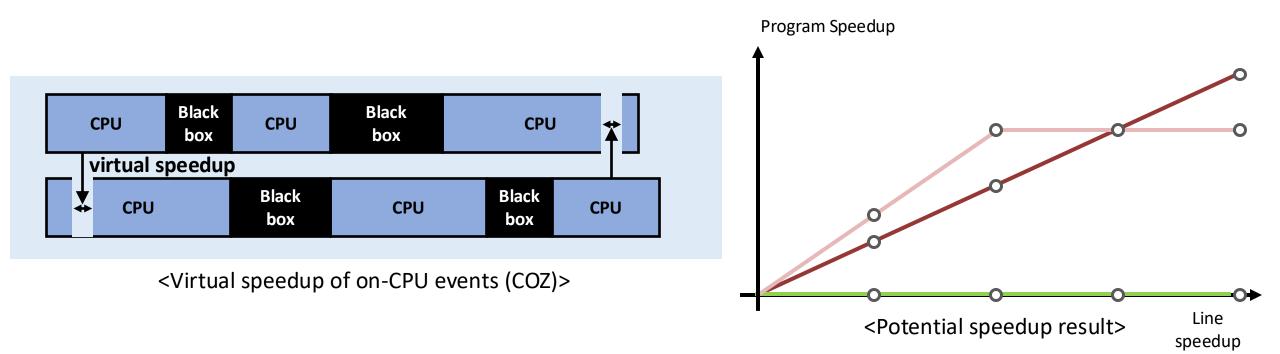


#### **Research Question**

- What if virtual speedup can be applied to I/O events
  - COZ has profiled on-CPU events only
- How to make COZ apply the virtual speedup idea to I/O events (or off-CPU events)
   E.g., disk I/Os

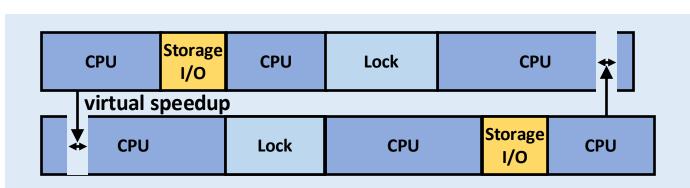
# **BCOZ: Causal Profiler for both On-/Off-CPU Events**

- Virtual speedup the off-CPU events by *blocked samples* 
  - Shows potential speedup when off-CPU events are optimized
    - Locks, I/O, scheduling delay, etc.

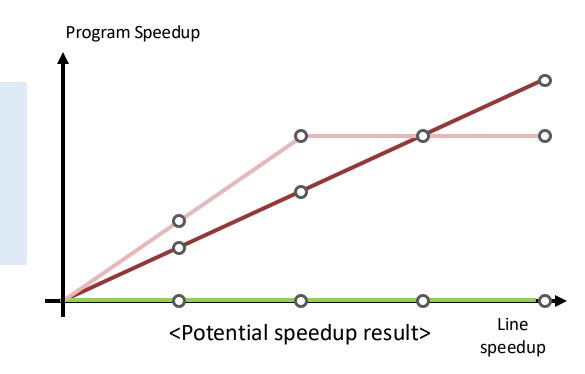


# **BCOZ: Causal Profiler for both On-/Off-CPU Events**

- Virtual speedup the off-CPU events by *blocked samples* 
  - Shows potential speedup when off-CPU events are optimized
    - Locks, I/O, scheduling delay, etc.

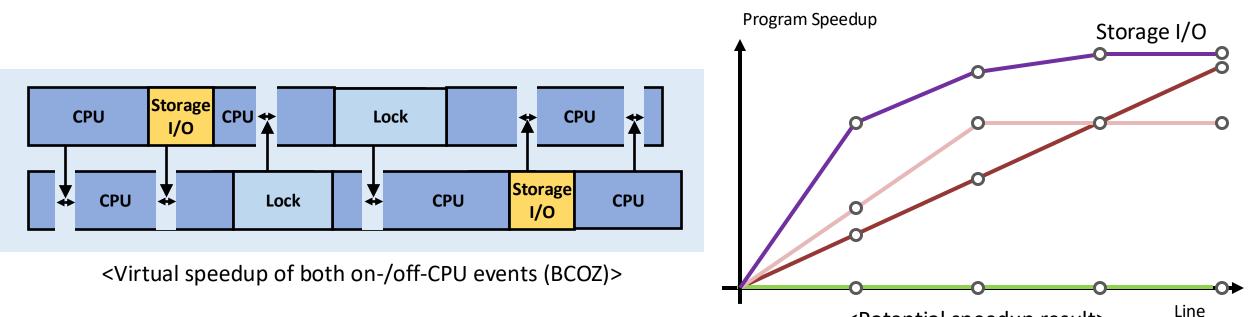


<Virtual speedup of both on-/off-CPU events (BCOZ)>



# **BCOZ: Causal Profiler for both On-/Off-CPU Events**

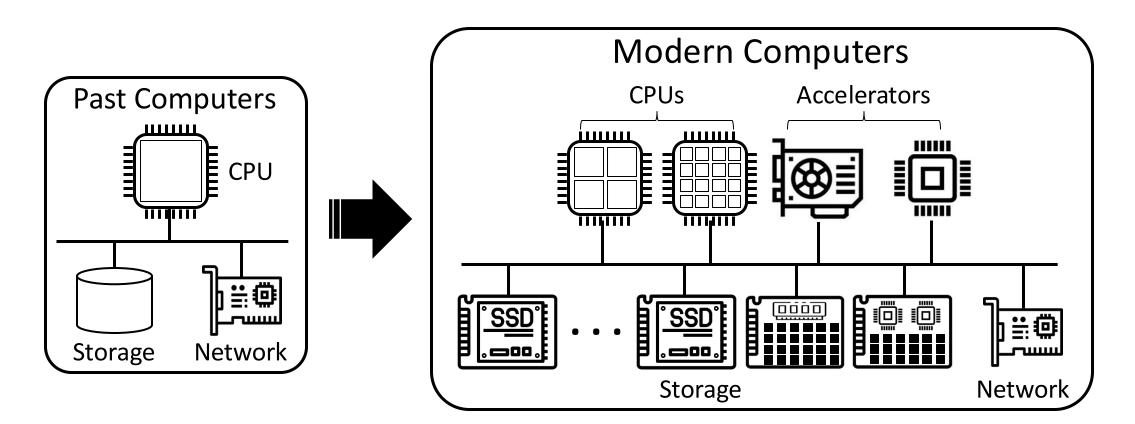
- Virtual speedup the off-CPU events by *blocked samples* 
  - Shows potential speedup when off-CPU events are optimized
    - Locks, I/O, scheduling delay, etc.



speedup

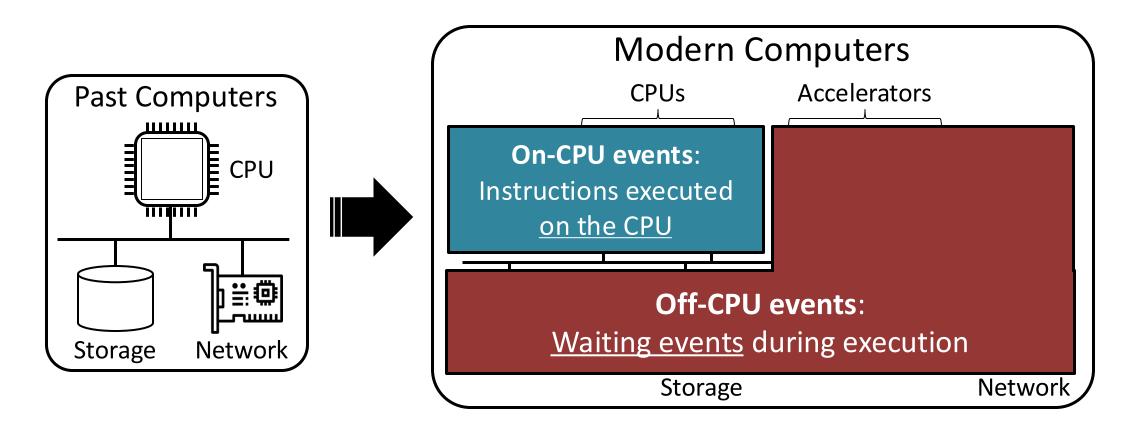
# **Trend of Computing Environments**

- Computing environments are becoming more complex and advanced
  - Events executed outside the CPU (i.e., off-CPU) have become more diverse



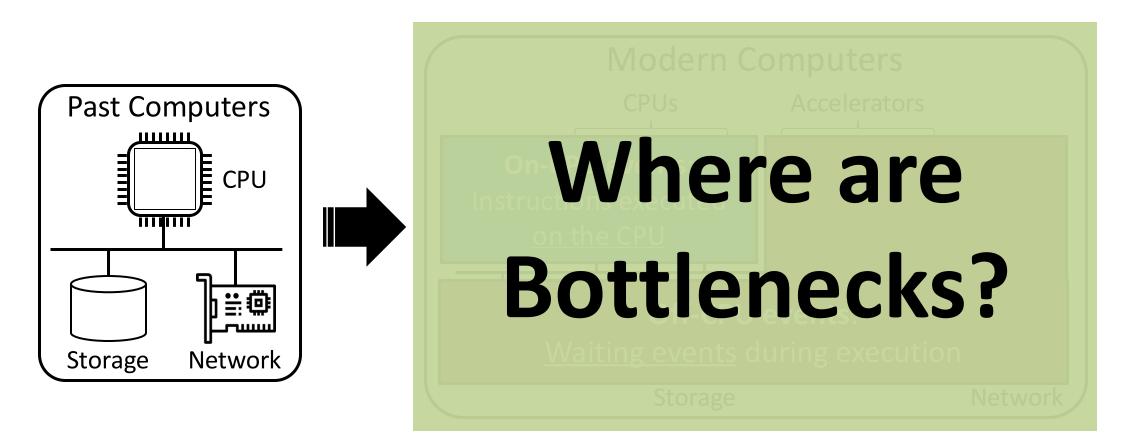
# **Trend of Computing Environments**

- Computing environments are becoming more complex and advanced
  - Events executed outside the CPU (i.e., off-CPU) have become more diverse

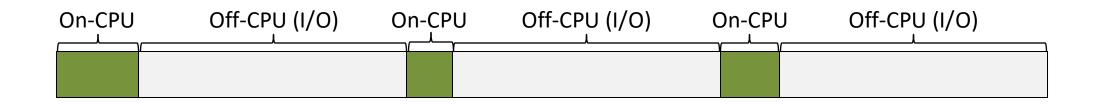


# **Trend of Computing Environments**

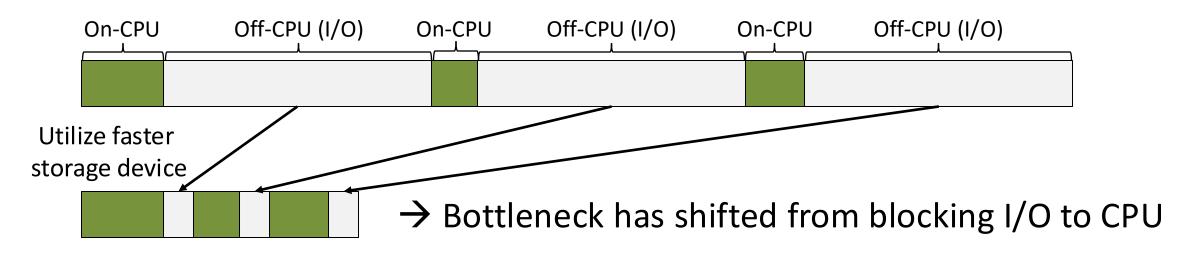
- Computing environments are becoming more complex and advanced
  - Events executed outside the CPU (i.e., off-CPU) have become more diverse



- Bottlenecks of applications are diversifying
  - (I/O) Boundary between CPU-bound and I/O-bound is blurred



- Bottlenecks of applications are diversifying
  - (I/O) Boundary between CPU-bound and I/O-bound is blurred

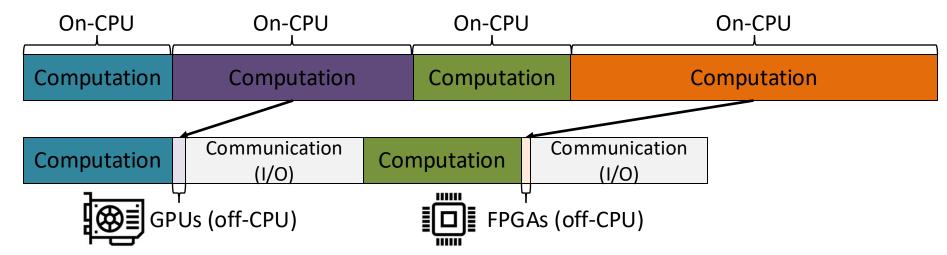


- "kernel software is becoming the bottleneck", XRP [OSDI '22]
- "server CPU is becoming the bottleneck", XSTORE [OSDI '20]
- "Rocksdb is CPU-bound", Kvell [SOSP '19]
- "kernel I/O stack accounts for a large fraction", AIOS [ATC '19]
- "storage no longer being the bottleneck", uDepot [FAST '19]

- Bottlenecks of applications are diversifying
  - (I/O) Boundary between CPU-bound and I/O-bound is blurred
  - (<u>Computation</u>) Shifting away from CPU-centric computations

On-CPU	On-CPU On-CPU		On-CPU		
Computation	Computation	Computation	Computation		

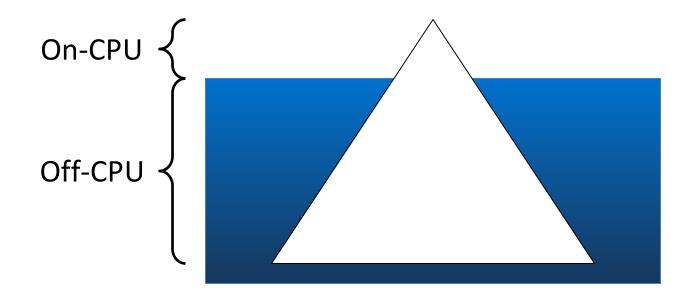
- Bottlenecks of applications are diversifying
  - (I/O) Boundary between CPU-bound and I/O-bound is blurred
  - (<u>Computation</u>) Shifting away from CPU-centric computations



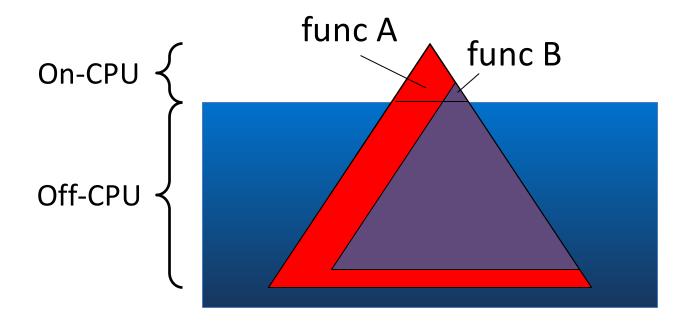
 $\rightarrow$  Bottleneck has shifted from CPU computation to I/O and communication

- "there are spare CPU and network bandwidth", BytePS [OSDI '20]
- "rapid increases in GPU will shift the bottleneck towards communication", PipeDream [SOSP '19]
- "DNN training is not scalable, mainly due to the communication overhead", ByteScheduler [SOSP '19]

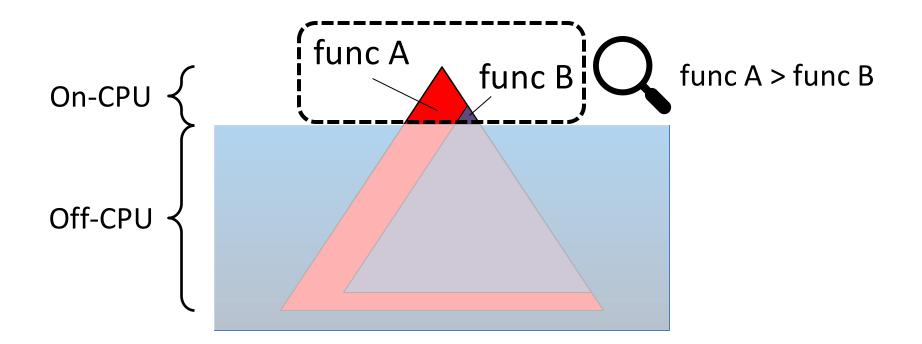
- Both on-CPU and off-CPU events need to be considered <u>simultaneously</u>
  - (Challenge #1) Analysis is conducted using only partial information



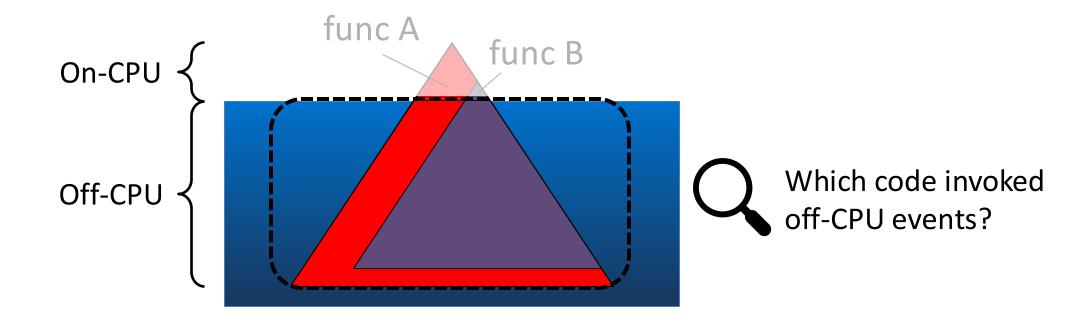
- Both on-CPU and off-CPU events need to be considered <u>simultaneously</u>
  - (Challenge #1) Analysis is conducted using only partial information



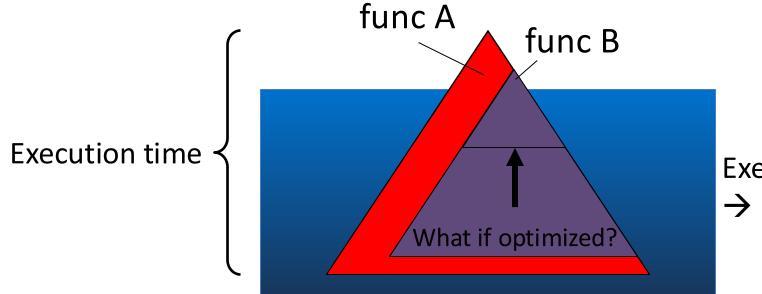
- Both on-CPU and off-CPU events need to be considered <u>simultaneously</u>
  - (Challenge #1) Analysis is conducted using only partial information



- Both on-CPU and off-CPU events need to be considered <u>simultaneously</u>
  - (Challenge #1) Analysis is conducted using only partial information

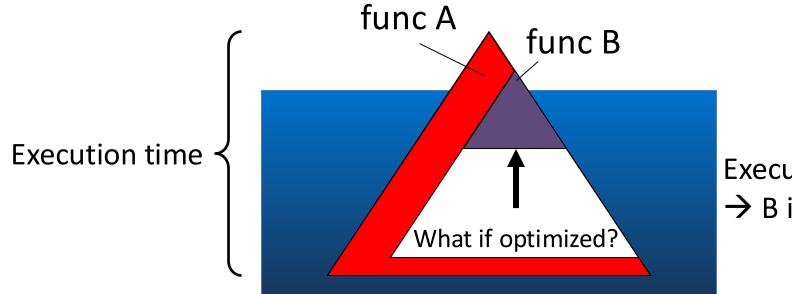


- Both on-CPU and off-CPU events need to be considered <u>simultaneously</u>
  - (Challenge #1) Analysis is conducted using only partial information
  - (Challenge #2) Hard to assess the impact of optimizing off-CPU events



Execution time is unchanged  $\rightarrow$  B is not on the critical path

- Both on-CPU and off-CPU events need to be considered <u>simultaneously</u>
  - (Challenge #1) Analysis is conducted using only partial information
  - (Challenge #2) Hard to assess the <u>impact of optimizing</u> off-CPU events

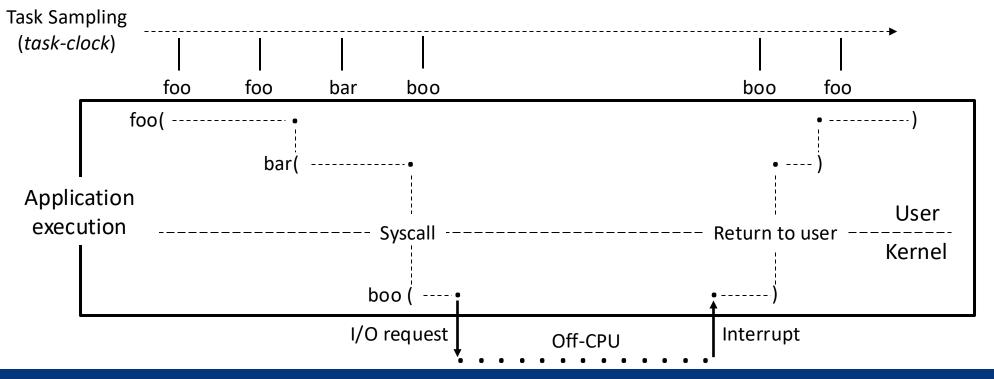


Execution time is unchanged  $\rightarrow$  B is not on the critical path

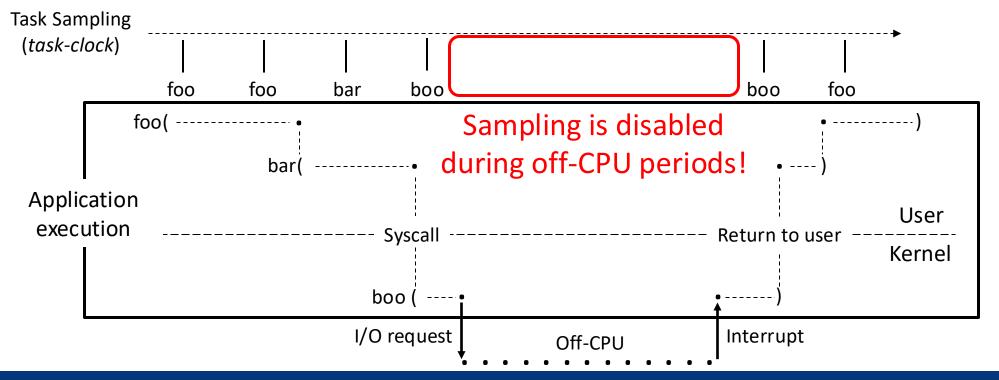
- Linux *perf* sampling (*task-clock*)
  - Feature in Linux kernel's perf subsystem
  - Collects profiling information (e.g., IP and callchain) periodically
  - A Low overhead, effective technique to analyze on-CPU behavior

S	amples: 1M	of event 'task-c	lock', Event count (	approx.):	: 1097249000000
	Overhead	Command	Shared Object	Symbol	
+			[kernel.vmlinux]		ive_queued_spin_lock_slowpath
-			libpthread-2.30.so	[L]บ	ll_lock_wait
		lll_lock_wait			
	pt	<pre>hread_mutex_lock</pre>			
	- rock	<pre>sdb::port::Mutex</pre>	::Lock		
	- 1	2.51% rocksdb::Ll	RUCacheShard::Lookup		
	rocksdb::ShardedCache::Lookup				
	<ul> <li>rocksdb::BlockBasedTable::GetEntryFromCache</li> </ul>				
	+ 8.05% rocksdb::BlockBasedTable::GetDataBlockFromCache <rocksdb::block></rocksdb::block>				
	+ 4.46% rocksdb::BlockBasedTable::GetDataBlockFromCache <rocksdb::parsedfullfilterblock></rocksdb::parsedfullfilterblock>				
	+ 1	1.55% rocksdb::Ll	RUCacheShard::Releas	e	
+	6.24%	db bench vanill	[kernel.vmlinux]	[k] raw	w spin unlock irgrestore

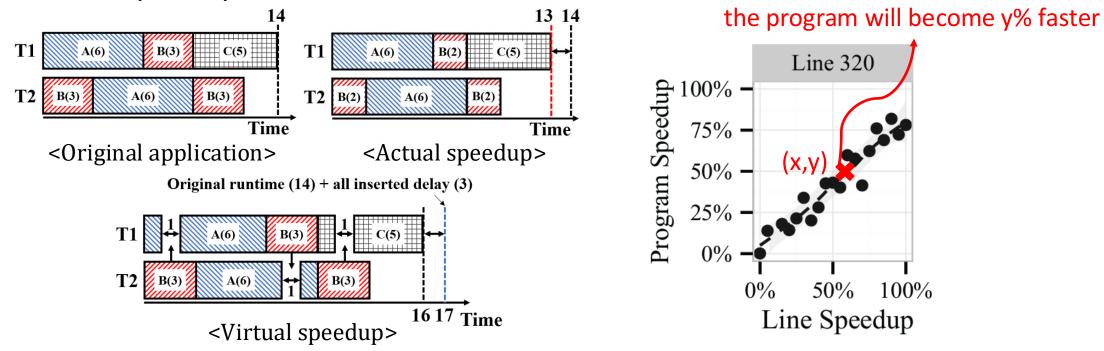
- Linux perf sampling (task-clock)
  - Feature in Linux kernel's perf subsystem
  - Collects profiling information (e.g., IP and callchain) periodically
  - A Low overhead, effective technique to analyze on-CPU behavior



- Linux perf sampling (task-clock)
  - Feature in Linux kernel's perf subsystem
  - Collects profiling information (e.g., IP and callchain) periodically
  - A Low overhead, effective technique to analyze on-CPU behavior



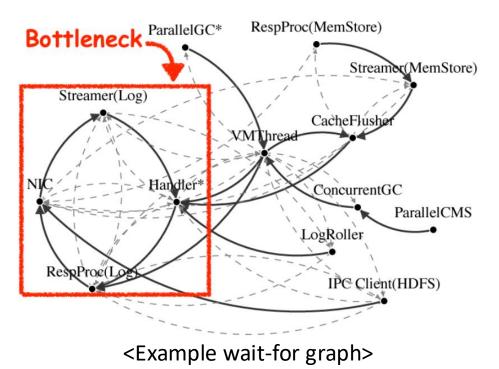
- COZ [SOSP '15]
  - Predict the impact of optimizing the specific code line without actual optimization
  - Virtual speedup



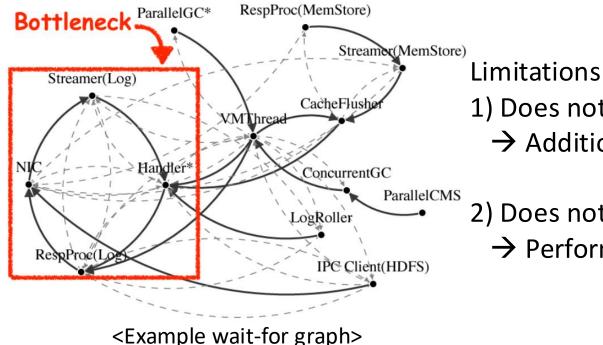
COZ utilizes on-CPU sampling (Linux *perf*)  $\rightarrow$  Virtual speedup is <u>limited to only on-CPU events</u>

If line 320 becomes x% faster,

- wPerf [OSDI '18]
  - Traces all kinds of waiting events including I/O and their dependencies
  - Wait-for graph: Dependency graph of executed threads
    - Identifying closed loops (i.e., knots) through graph analysis



- wPerf [OSDI '18]
  - Traces all kinds of waiting events including I/O and their dependencies
  - Wait-for graph: Dependency graph of executed threads
    - Identifying closed loops (i.e., knots) through graph analysis



- 1) Does not provide <u>context information</u> of the bottleneck
  - $\rightarrow$  Additional effort is needed to determine where to optimize
- 2) Does not provide the actual impact of optimization
  - $\rightarrow$  Performance gain of the optimization could be marginal

#### **Summary of the Limitations**

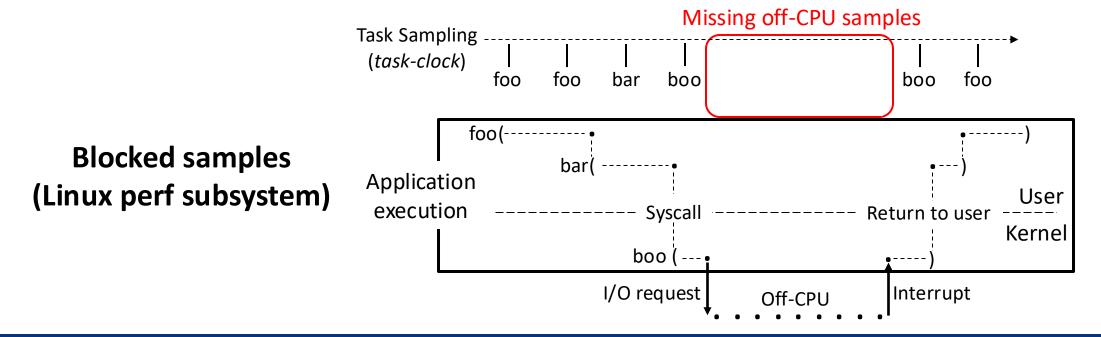
 $\rightarrow$  (Limitation #1) Focuses solely on either on-CPU or off-CPU events

→ (Limitation #2) Causality analysis is not supported for off-CPU events

Profiler	Profiling Scope	Causality Analysis	
Linux perf	On-CPU	X	
COZ	UN-CPU	<u> </u>	
wPerf	Off-CPU	X	
Blocked Samples	Both on-/off-CPU	Ο	

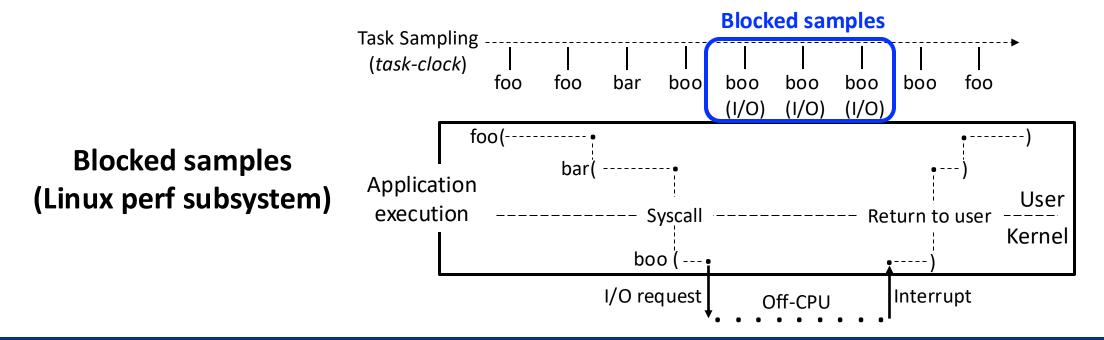
# **Our Approach: Blocked Samples**

• Goal: sampling on- and off-CPU events simultaneously



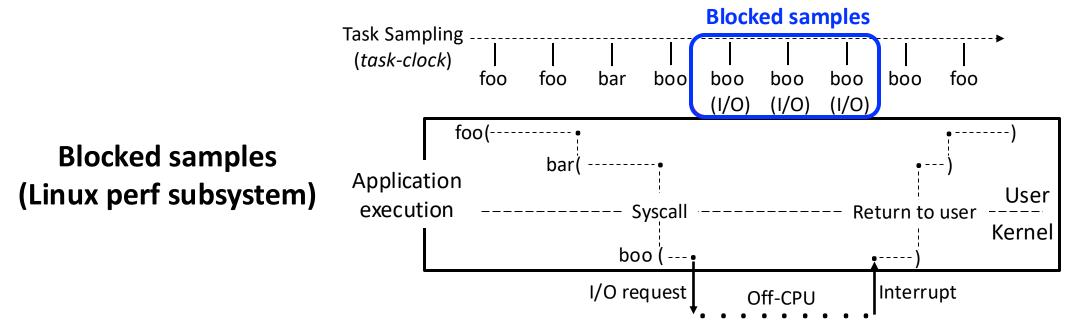
# **Our Approach: Blocked Samples**

- Goal: sampling on- and off-CPU events simultaneously
  - Blocked samples: sampling technique for off-CPU events



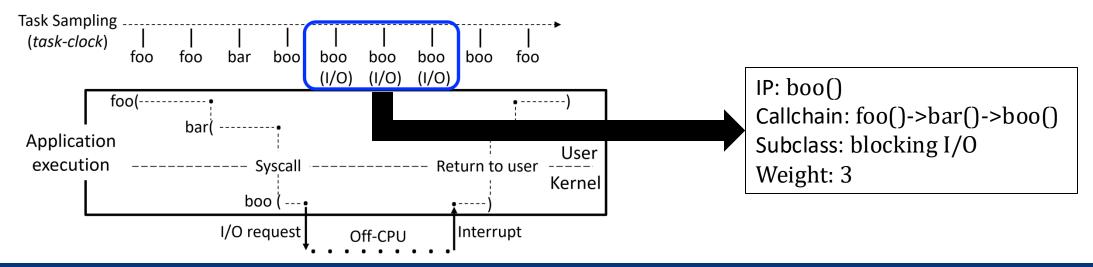
# **Our Approach: Blocked Samples**

- Goal: sampling on- and off-CPU events simultaneously
  - Blocked samples: sampling technique for off-CPU events
  - Proposed profilers using blocked samples
    - **bperf**: sampling-based <u>statistical profiler</u> on both on-/off-CPU events
    - BCOZ: causal profiler that supports virtual speedup on both on-/off-CPU events



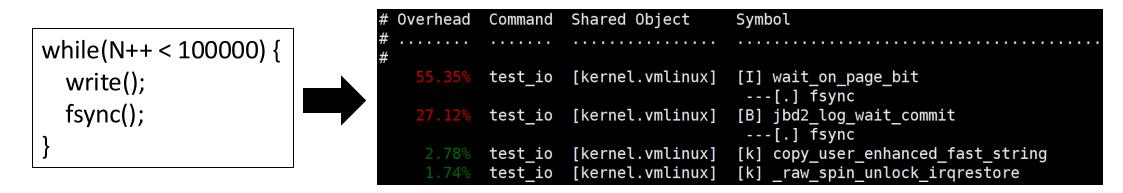
# **Blocked Samples**

- Collected information
  - IP and callchain
  - Off-CPU subclass: reason for the blocking
    - Blocking I/O, synchronization, CPU scheduling, etc.
      - New subclasses can be defined as needed
  - Weight: # of repeats
    - Encode the number of blocked samples with the same attributes



# *bperf*: Statistical Profiler on Both On-/Off-CPU Events

- Extension of Linux perf tool to support blocked samples
  - Sample accounting
  - Result reporting
    - [I]: blocking I/O, [L]: synchronization, [S]: CPU scheduling, [B]: others
    - Both the last user-level IP and last kernel-level IP are reported for blocked samples
      - Enables an in-depth understanding of off-CPU events



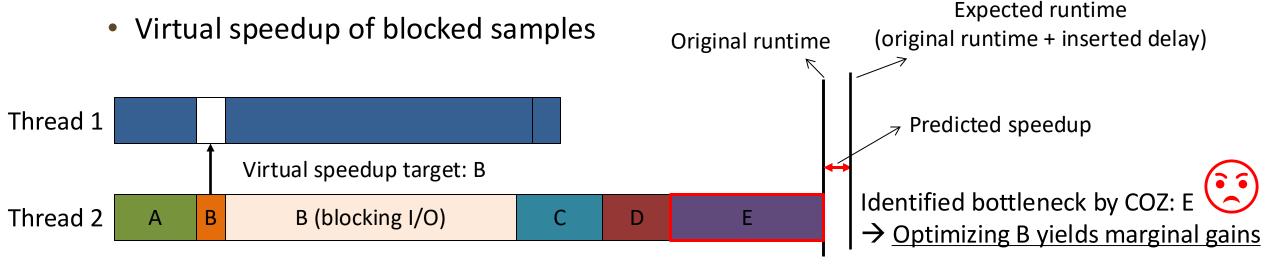
# *bperf*: Statistical Profiler on Both On-/Off-CPU Events

- Extension of Linux perf tool to support blocked samples
  - Sample accounting
  - Result reporting
    - [I]: blocking I/O, [L]: synchronization, [S]: CPU scheduling, [B]: others
    - Both the last user-level IP and last kernel-level IP are reported for blocked samples
      - Enables an in-depth understanding of off-CPU events



# **BCOZ:** Causal Profiler on Both On-/Off-CPU Events

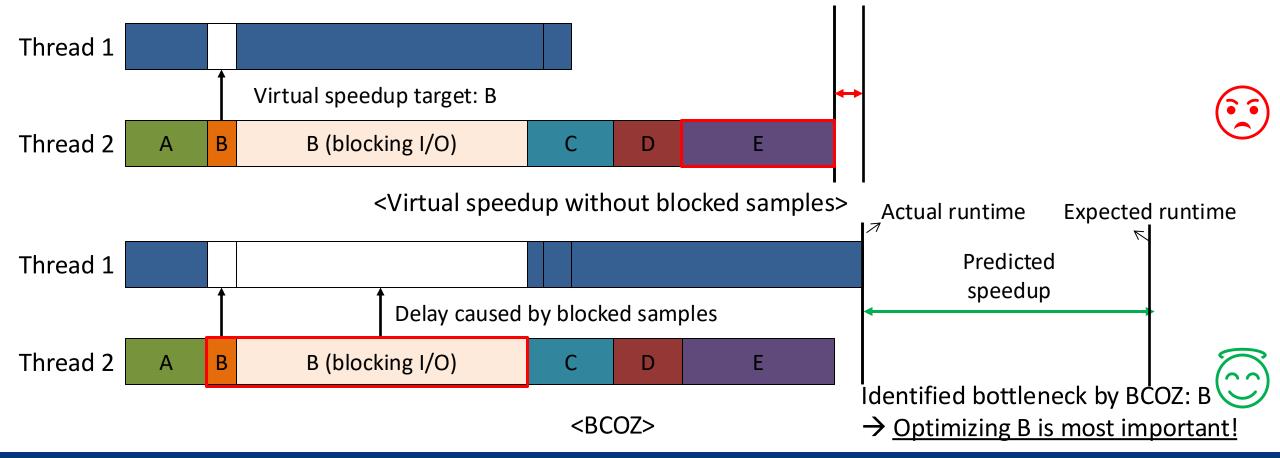
• Extension of COZ to support blocked samples



<Virtual speedup without blocked samples>

# **BCOZ:** Causal Profiler on Both On-/Off-CPU Events

- Extension of COZ to support blocked samples
  - Virtual speedup of blocked samples



# **Features and Challenges of BCOZ**

- Features
  - Sampling kernel codes
  - Virtual speedup of blocked samples
  - Subclass-level virtual speedup
- Challenges
  - Conflicts with optimization of original COZ
    - Dependency handling + batch processing of samples

#### $\rightarrow$ For more details, please refer to the paper

#### **Experimental Setup**

- CPU: Intel Xeon Gold 5218 2.30GHz \* 2
- OS: Ubuntu 20.04 Server (Linux kernel version: 5.3.7)
- Memory: DDR4 2933MHz, 384GB
- Storage devices: Samsung NVMe PM1735 (1,500K IOPS)
- Questions:
  - Q1) Can blocked samples identify true bottlenecks?
  - Q2) Differences from wPerf's results?
  - Q3) Profiling overhead?
    - Comparison of tracing (off-CPU only), sampling (on-CPU only), bperf (both on-/off-CPU)
    - BCOZ overhead analysis
  - $\rightarrow$  Please refer to the paper

# **Summary of the Profiling Results**

#### • Results included in the paper

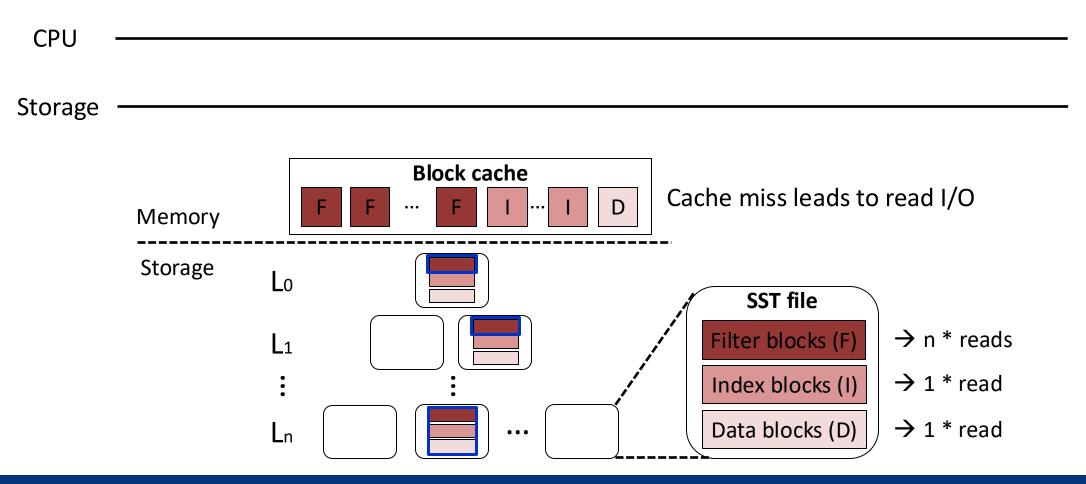
Benchmark	Workload	Identified bottlenecks	Optimization	Speedup?	Known solution?	
	prefix_dist	Block cache contention	- Sharding	O (3.4x)	Yes	Case study 2
	allrandom	Block read I/O	- Asynchronous I/O	O (1.8x)	No	Case study 1
RocksDB	fillrandom	Compaction, write stall	<ul> <li>No block compression</li> <li>Increase the number of</li> <li>compaction thread</li> <li>Reduce write stall</li> </ul>	O (2.6x)	Yes	
NPB	Integer sort	CPU contention	- Allocate more CPU cores	O (16.4x)	Yes	

• Results not included in the paper (optimization is ongoing)

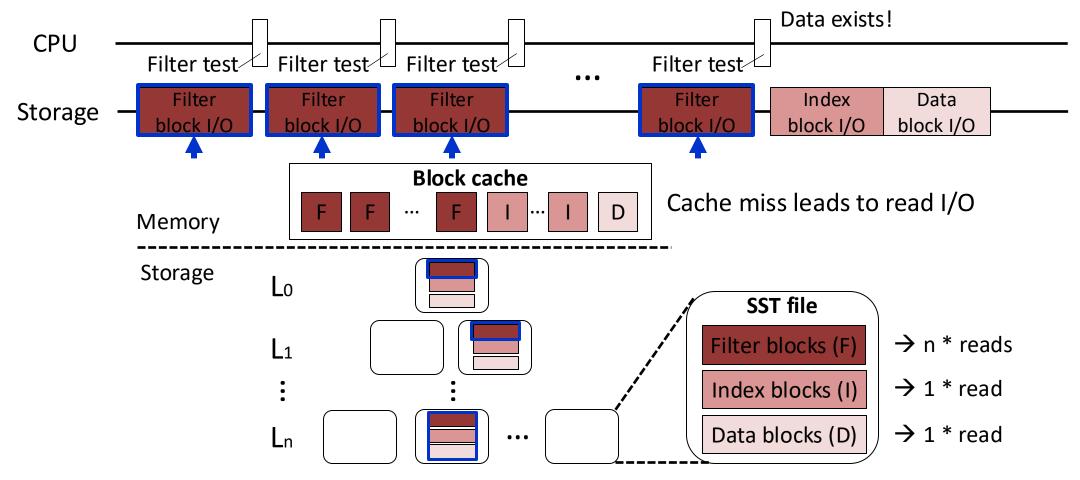
Benchmark	Identified Bottlenecks	
HPCG	Serialized SYMGS (Symmetric Gauss Seidel) kernel	
LLaMA-cpp	Blocking I/O in ggml_vec_dot	

# **Case Study 1– RocksDB (Block Read Operation)**

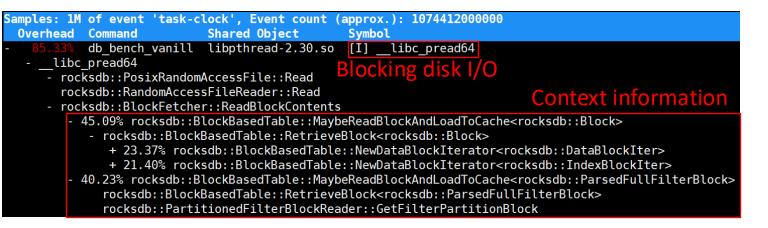
- Scenario: read-only workload (*allrandom*), small block cache (0.1% of dataset size)
- Problem: frequent block (filter, index, data) read I/Os



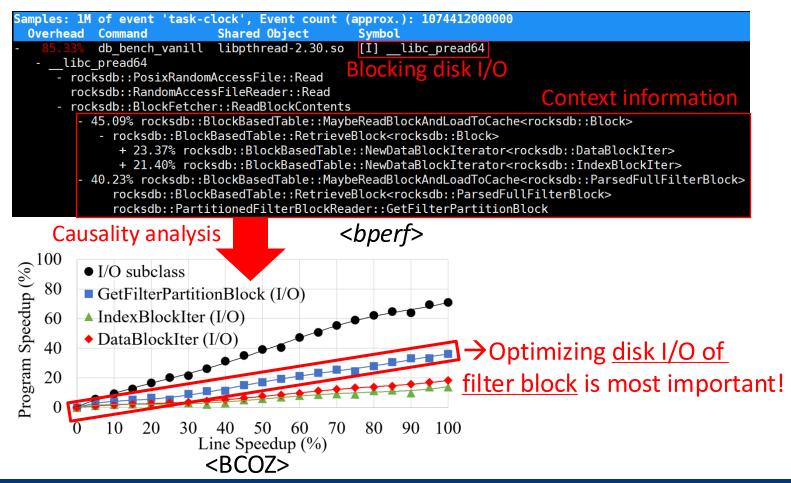
- Scenario: read-only workload (allrandom), small block cache (0.1% of dataset size)
- Problem: frequent block (filter, index, data) read I/Os



- Scenario: read-only workload (*allrandom*), small block cache (0.1% of dataset size)
- Identified bottlenecks: blocking disk I/O (filter, index, and data blocks)

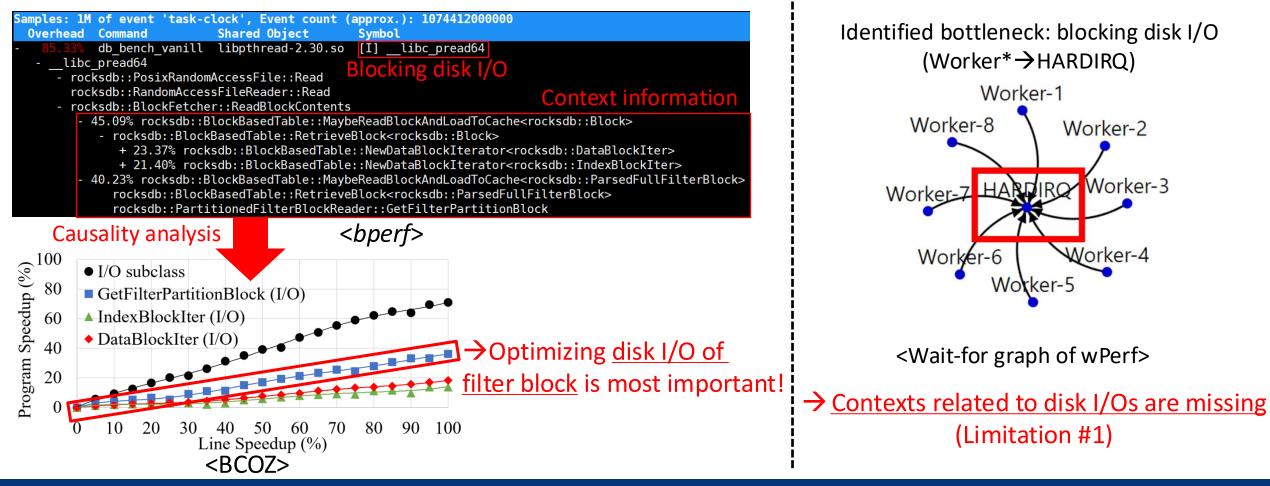


- Scenario: read-only workload (allrandom), small block cache (0.1% of dataset size)
- Identified bottlenecks: blocking disk I/O (filter, index, and data blocks)

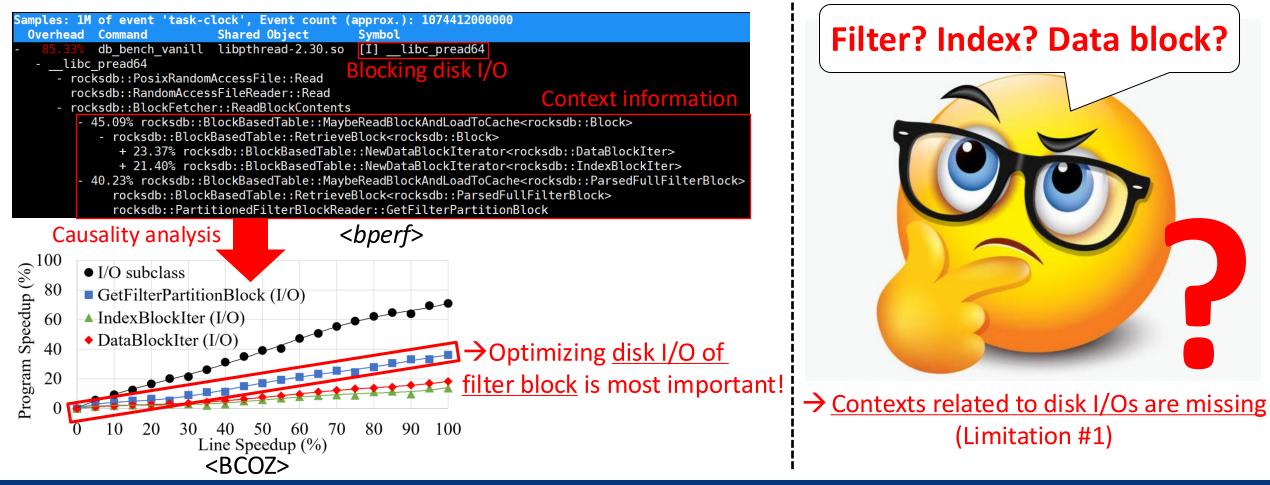


Jinkyu Jeong, NVRAMOS 2024

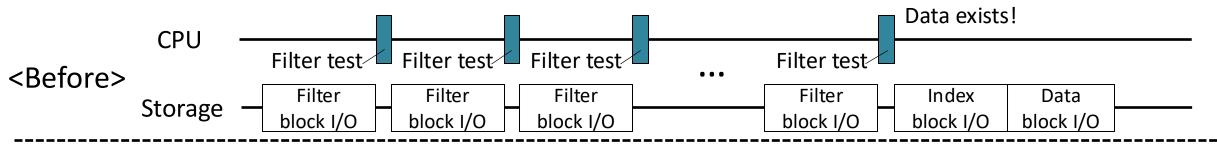
- Scenario: read-only workload (allrandom), small block cache (0.1% of dataset size)
- Identified bottlenecks: blocking disk I/O (filter, index, and data blocks)



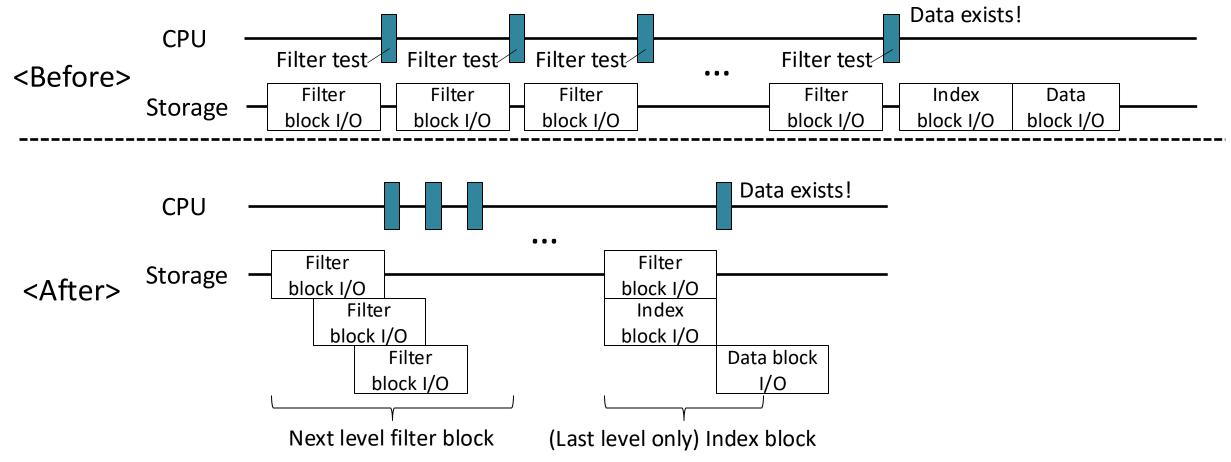
- Scenario: read-only workload (allrandom), small block cache (0.1% of dataset size)
- Identified bottlenecks: blocking disk I/O (filter, index, and data blocks)



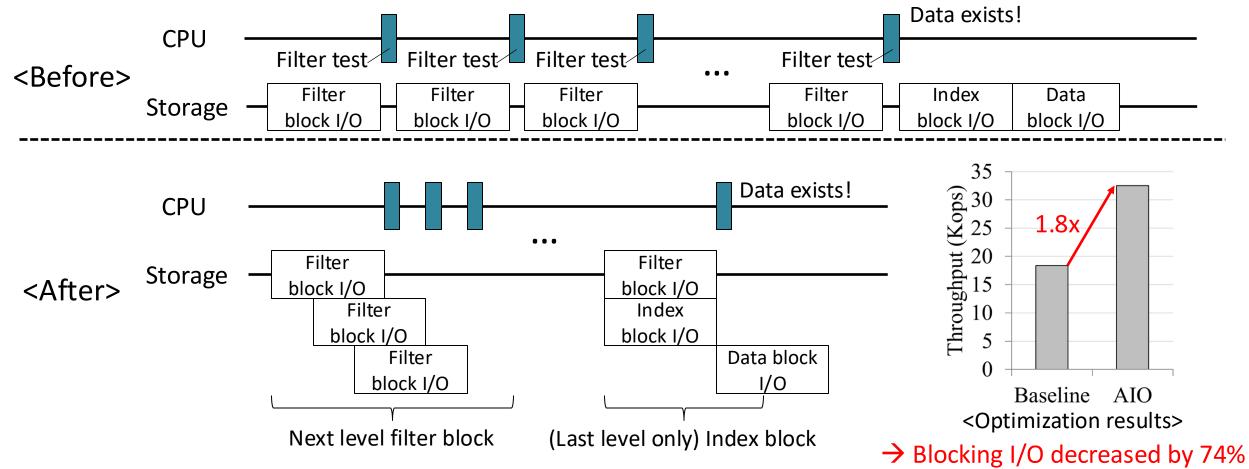
- Scenario: read-only workload (*allrandom*), small block cache (0.1% of dataset size)
- Optimization: asynchronous I/O for filter and index blocks



- Scenario: read-only workload (allrandom), small block cache (0.1% of dataset size)
- Optimization: asynchronous I/O for filter and index blocks

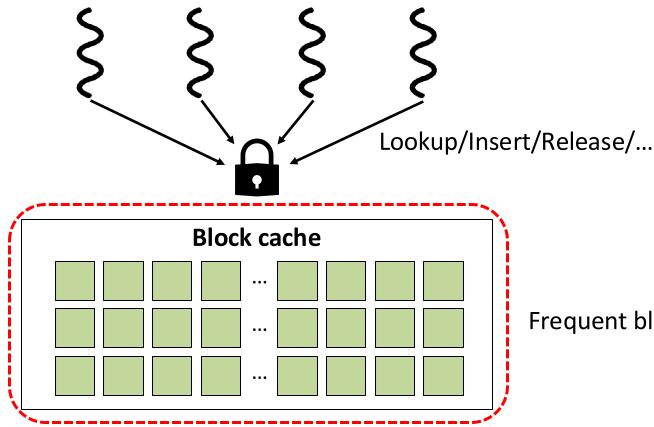


- Scenario: read-only workload (allrandom), small block cache (0.1% of dataset size)
- Optimization: asynchronous I/O for filter and index blocks



#### **Case Study 2 – RocksDB (Block Cache Contention)**

- Scenario: read-only workload (*prefix\_dist*), large block cache (10% of dataset size)
- Problem: block cache lock contention



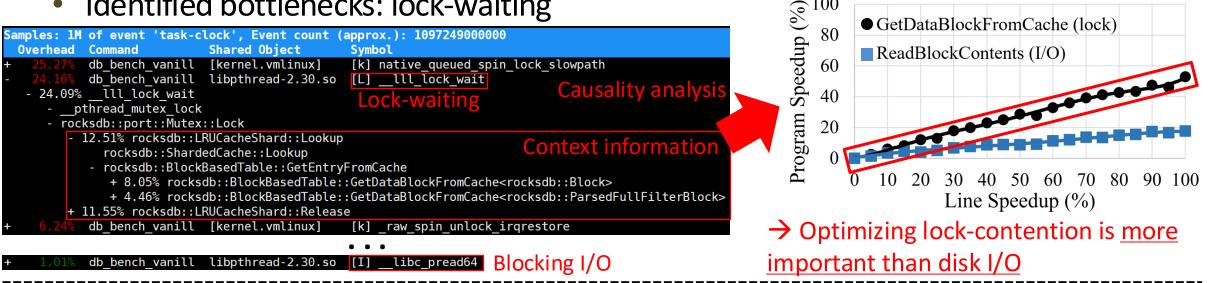
Frequent block cache access leads to lock contention

# **Case Study 2– RocksDB (Block Cache Contention)**

Scenario: read-only workload (*prefix\_dist*), large block cache (10% of dataset size)

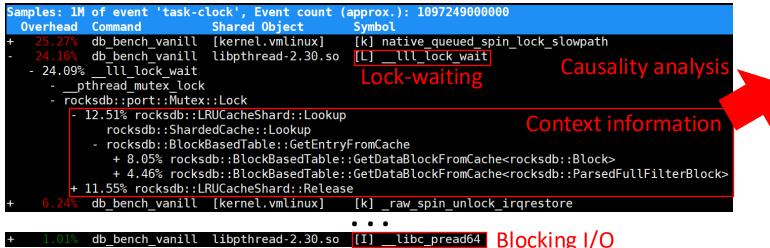
100

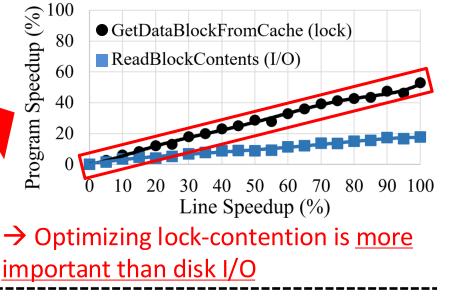
Identified bottlenecks: lock-waiting 



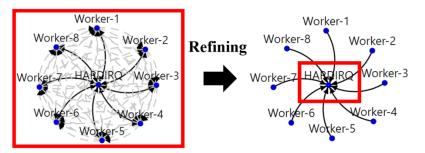
# **Case Study 2- RocksDB (Block Cache Contention)**

- Scenario: read-only workload (*prefix\_dist*), large block cache (10% of dataset size)
- Identified bottlenecks: lock-waiting





# Identified bottleneck: blocking disk I/O, lock-waiting (Worker\* $\rightarrow$ HARDIRQ, Worker\* $\leftarrow$ $\rightarrow$ Worker\*)



(Limitation #1)

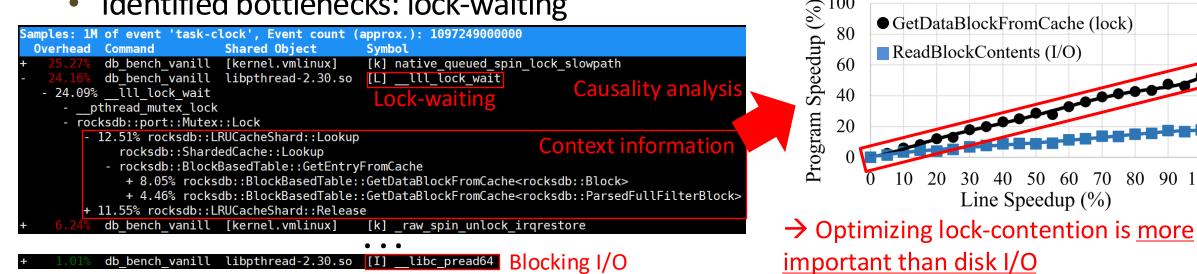
 $\rightarrow$  Codes that invoke lock-contention are missing

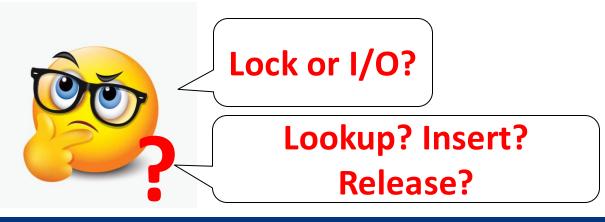
(Limitation #2)

 $\rightarrow$  Actual impact of optimizing blocking disk I/O is missing

# **Case Study 2– RocksDB (Block Cache Contention)**

- Scenario: read-only workload (*prefix\_dist*), large block cache (10% of dataset size)
- Identified bottlenecks: lock-waiting





(Limitation #1)

 $\rightarrow$  Codes that invoke lock-contention are missing

100

(Limitation #2)

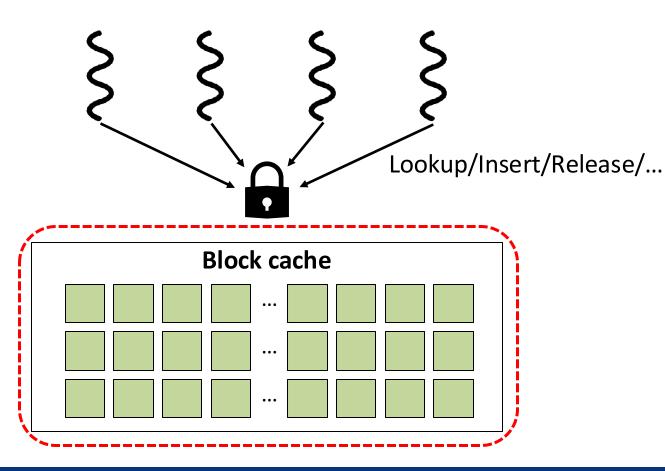
 $\rightarrow$  Actual impact of optimizing blocking disk I/O is missing

----

70 80 90 100

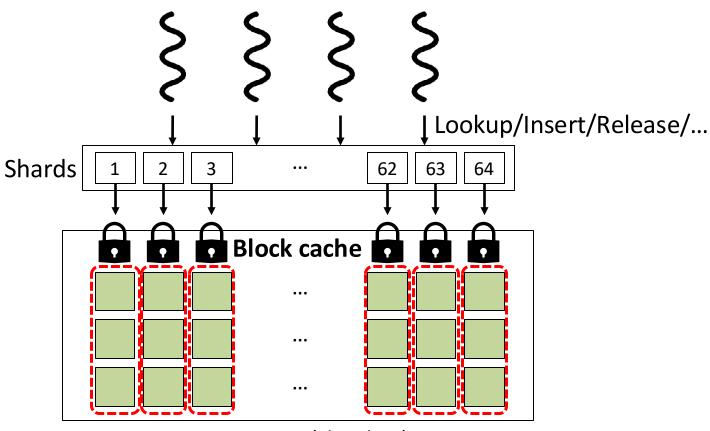
# **Case Study 2- RocksDB (Block Cache Contention)**

- Scenario: read-only workload (*prefix\_dist*), large block cache (10% of dataset size)
- Optimization: apply sharding



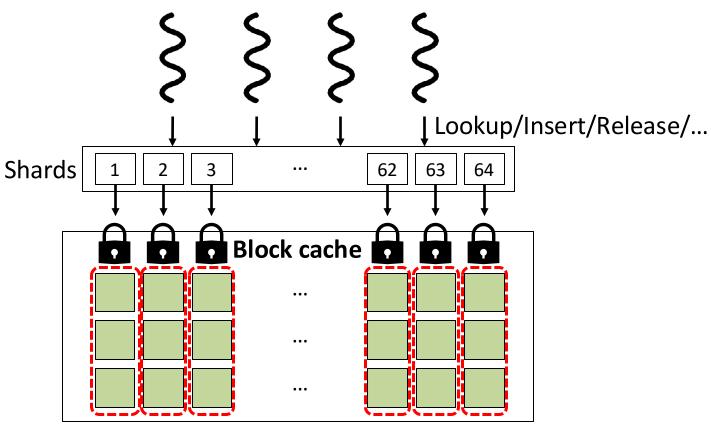
#### **Case Study 2- RocksDB (Block Cache Contention)**

- Scenario: read-only workload (*prefix\_dist*), large block cache (10% of dataset size)
- Optimization: apply sharding

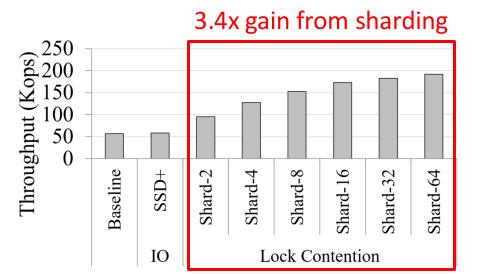


#### **Case Study 2– RocksDB (Block Cache Contention)**

- Scenario: read-only workload (*prefix\_dist*), large block cache (10% of dataset size)
- Optimization: apply sharding



<Optimization (sharding)>

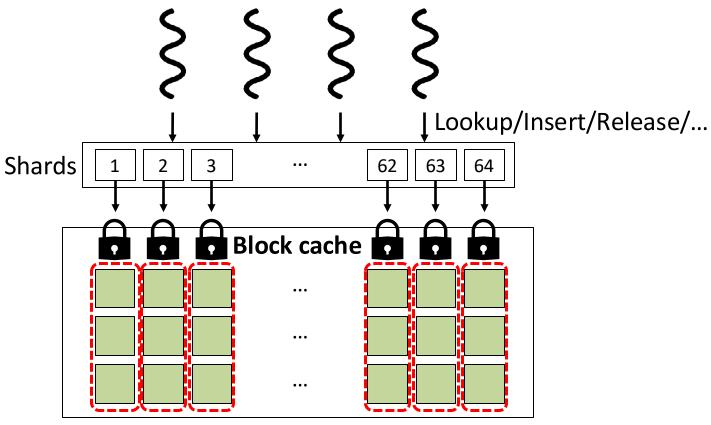


 $\rightarrow$  Lock-contention decreased by 97%

<Optimization results>

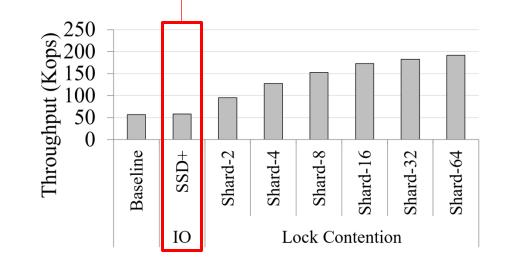
#### **Case Study 2- RocksDB (Block Cache Contention)**

- Scenario: read-only workload (*prefix\_dist*), large block cache (10% of dataset size)
- Optimization: apply sharding



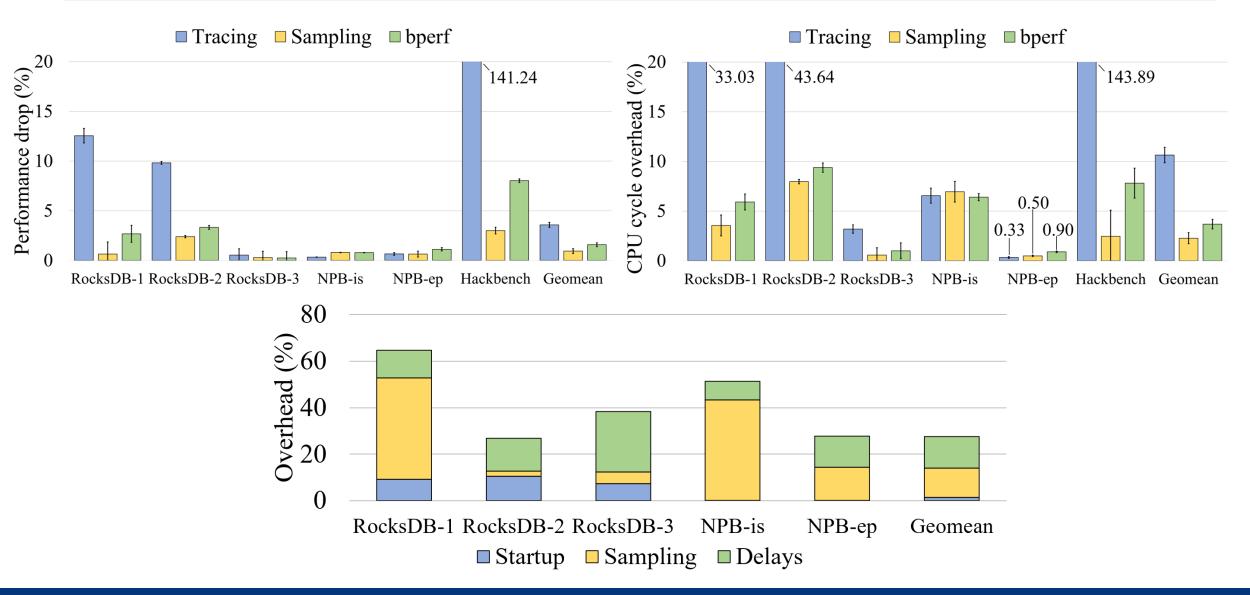
<Optimization (sharding)>





<Optimization results>

#### **Profiling Overhead**



#### Conclusion

- Profiling modern applications has become more challenging
- Blocked samples collects off-CPU events information
  - *bperf*, provides <u>statistical profiling</u> of both on-/off-CPU events
  - **BCOZ**, provides <u>virtual speedup</u> of both on-/off-CPU events
- Blocked samples, a general solution for off-CPU sampling
  - Planning on <u>enriching blocked samples</u> with off-CPU information details (device-internal ops., remote ops.)

Blocked samples is available at:

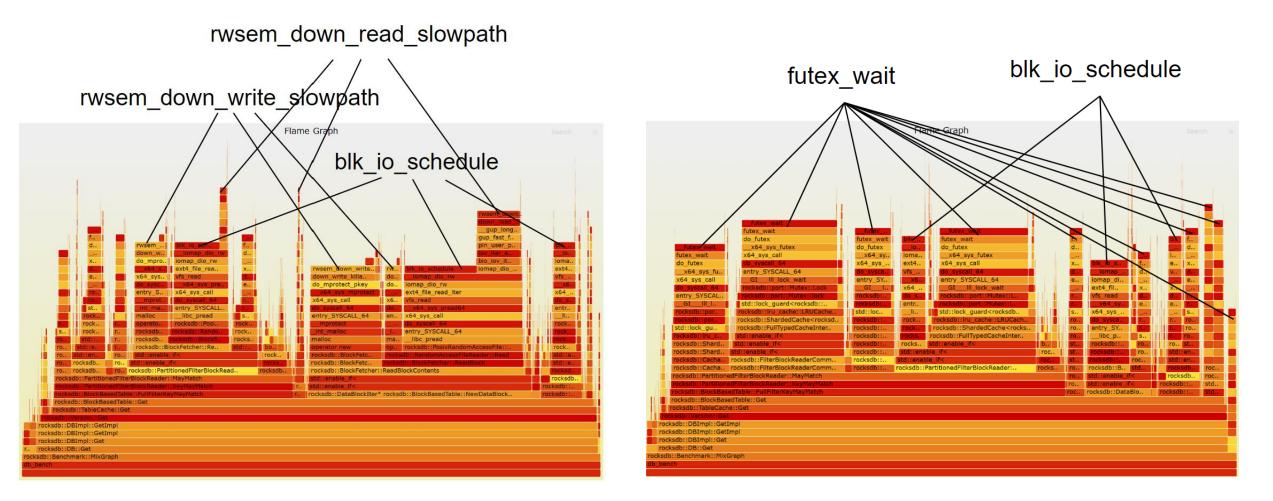
https://github.com/s3yonsei/blocked\_samples





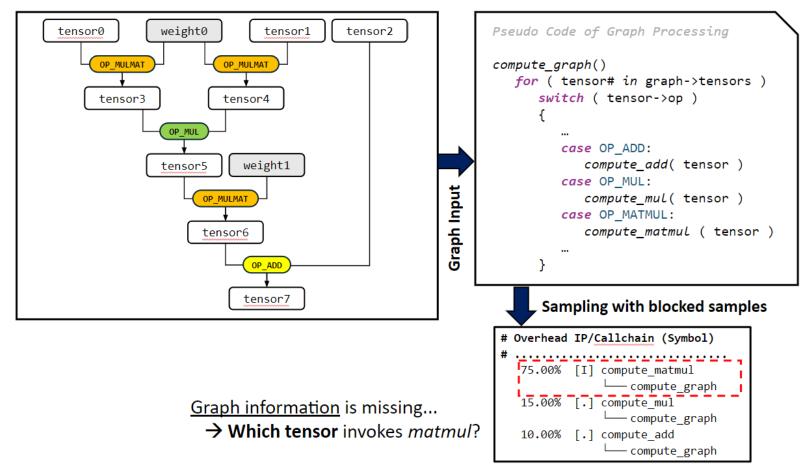
#### **Appendix: FlameGraph with Blocked Samples**

• Callchain visualization of both on-/off-CPU events



#### **Future Research Questions**

- Q1) Does <u>code context</u> is enough to understand bottleneck?
  - e.g., graph-processing applications



#### **Future Research Questions**

• Q2) What if there is nothing to optimize?

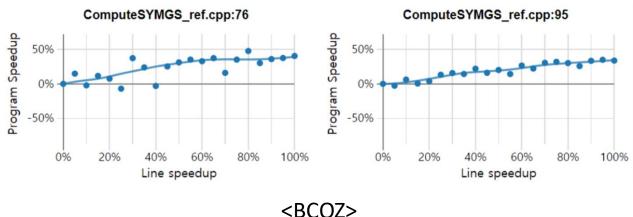


→ Optimizing any single event does not improve performance
 → Does that mean there is no room for further optimization?
 → Optimizing both {A, E} can improve the performance

# **Appendix**

#### **Case Study – HPCG (Serialized SYMGS Kernel)**

- Scenario: 64 application threads on 64 logical cores
- Identified bottlenecks: computation
  - ComputeSYMGS\_ref (symmetric gauss seidel kernel)
- Needed optimization: parallelize the SYMGS kernel execution



#### Identified bottlenecks in SYMGS code

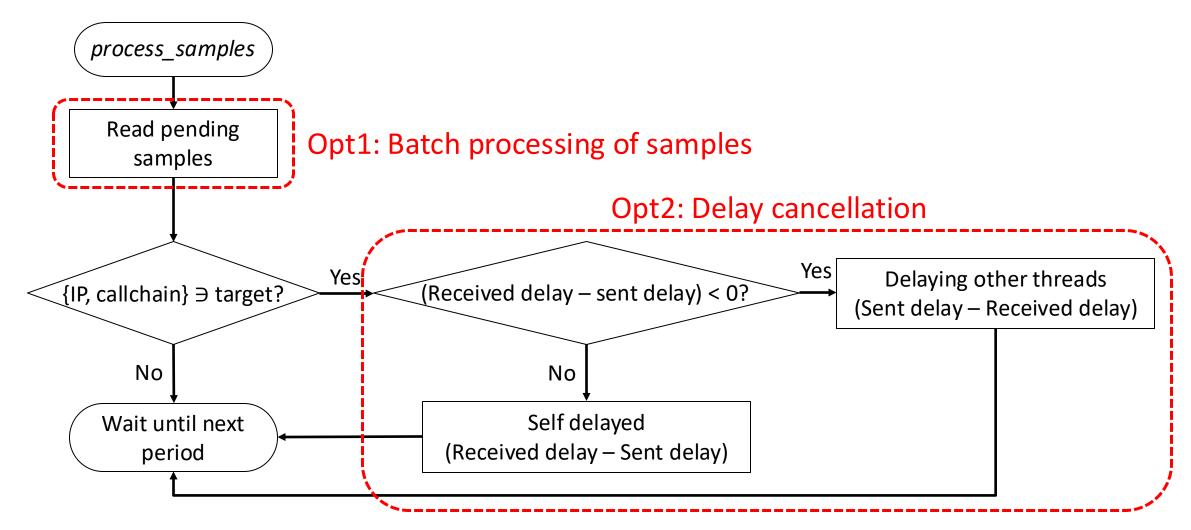
74	<pre>for (int j=0; j&lt; currentNumberOfNonzeros; j++) {</pre>
75	local_int_t curCol = currentColIndices[j];
76	<pre>sum -= currentValues[j] * xv[curCol];</pre>
77	}

3	<pre>for (int j = 0; j&lt; currentNumberOfNonzeros; j++) {</pre>
4	local_int_t curCol = currentColIndices[j];
5	<pre>sum -= currentValues[j]*xv[curCol];</pre>
6	}

#### <ComputeSYMGS\_ref.cpp>

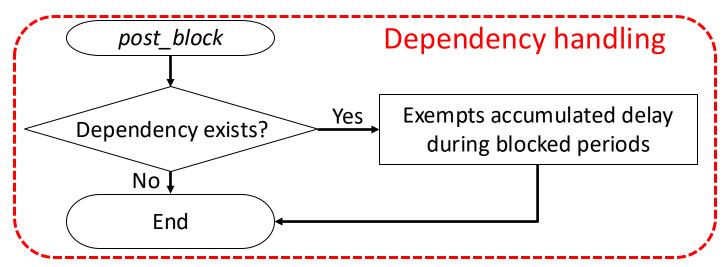
#### Implementation of COZ

• process\_samples: periodic virtual speedup operation

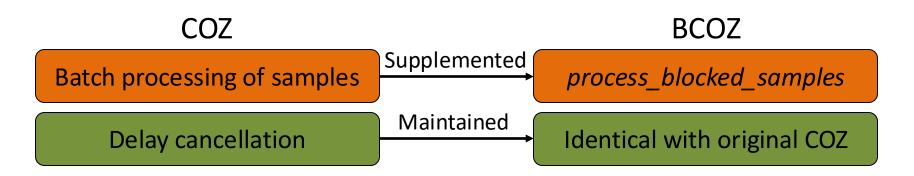


# (cont'd) Implementation of COZ

• *post\_block*: Delay exemption operation triggered at thread wakeup

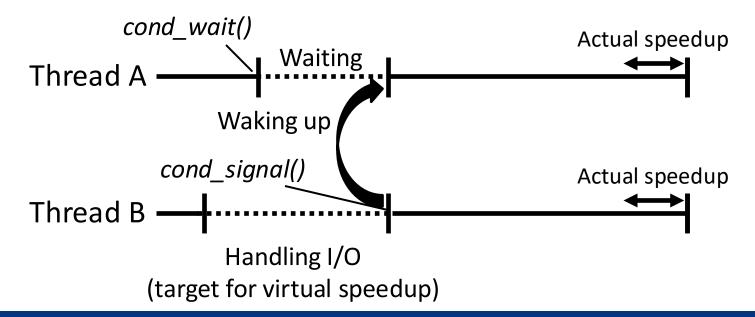


→ However, batch processing of *blocked samples* can compromise the dependency handling



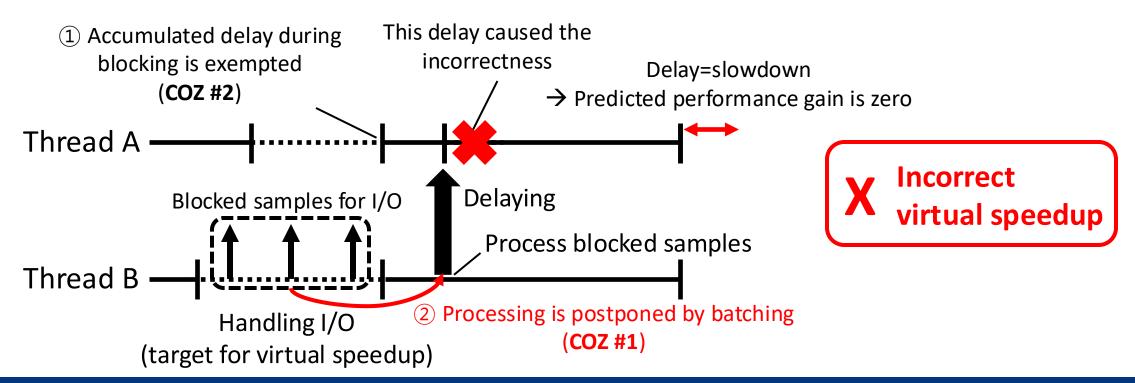
#### **Virtual Speedup of Blocked Samples**

- BCOZ handles dependencies between off-CPU events
  - Events with dependencies <u>cannot be sped up independently</u>



#### **Virtual Speedup of Blocked Samples**

- BCOZ handles dependencies between off-CPU events
  - Events with dependencies <u>cannot be sped up independently</u>
  - Batch processing of samples can cause inaccurate virtual speedup to occur after wakeup



#### **Virtual Speedup of Blocked Samples**

- BCOZ handles dependencies between off-CPU events
  - Events with dependencies cannot be sped up independently
  - Batch processing of samples can cause inaccurate virtual speedup to occur after wakeup
  - BCOZ processes blocked samples immediately when a thread wakes up another thread

